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Due to lack of space, the MPG article has been postponed until the June issue.
Hot water from the freezer

The price of energy has risen considerably during the past few years and it certainly does not look as if this is ever likely to drop. It is time to take steps to conserve energy in a field which, until recently, attracted attention on an academic level only.

Current consumption in the freezer

Nowadays, about 47% of homes in Britain own a freezer. Recently there has been a drop in annual sales. This is because freezers consume a great deal of current. How does this compare with other household appliances? Figure 1 shows the general energy requirements in the home. 84% is used by primary energy sources such as natural gas or coal and by secondary energy sources such as electricity, coke or oil fuel. 10% is required to heat water. The country's 450 million electrical household appliances including TV, radio sets and lighting only consume 6% between them.

So although household appliances use up a relatively small percentage of the total energy consumption, industry is justified in its efforts to cut power requirements. In 1978 25% of the total went on domestic electricity. Figure 2 shows the three appliances which consume most electricity in the kitchen. 17% alone is used up by cooling and freezing equipment. Although their compressors require no more than 100 to 150 W, the huge amount of energy consumed is explained by the fact that they are almost continuously switched on. The total current consumption may be divided equally between the fridge and the freezer. The consumption of the freezer alone (5,5 GWh a year) corresponds to that of agricultural machinery or — to name another example — to about 70% of the railway and traffic requirements. It would, therefore, make a considerable difference if a freezer's energy could be 'frozen'. Even if only part of it could be transferred in the form of domestic heat, it would be a step in the right direction from an economic point of view.

Figure 3 shows the rising energy curve of fridges and freezers. The latter are usually more economical, because they are better insulated.

Balance of energy and heat transfer

A freezer with a 300 litre capacity — about the size recommended for a family of four — uses up an average of 2.3 kWh a day. It is transformed into heat across the compressor and cooling circuit. The balance of energy is shown

Figure 1. The use of energy in the household.

Figure 2. Current consumption in the home.

Figure 3. Energy consumed by freezers.
in figure 4. The electricity consumption \( Q_N \) is used as a 100% rating. By means of the evaporator \( Q_Q \) is drawn from the inside. The amount normally corresponds to the heat which penetrates the inside of the freezer through the door seals etc. The theoretical condensation heat \( Q_C \) represents the entire heat quantity as 183%. 90% of it is heat emission \( Q_Y \) in the condenser, 93% is heat emission \( Q_K \) produced by the compressor.

The balance of energy shows that it would be quite simple to transfer part of the heat emitted to boil water. For this purpose the condenser is replaced by a condenser spiral in a hot water tank, so that heat \( Q_Y \) may be directly used to heat water. An additional heating system ensures that water is heated to the required temperature or to that needed during a short period or consumption peak.

The cross-section in figure 5 shows how such a system is built up. The freezer is connected to the boiler by means of tubes along which the refrigeration substance circulates. In this way, 90% of the electrical energy extracted from the freezer may be used to heat water until a temperature of about 60°C. With the aid of a simple technique it is possible to increase the level of heat by 100% or more. This because the system operates as a water pump.

In this process the compressor plays an important part. If it were insulated from the outside air, an oil cooler could be installed to reclaim 80% of the heat. The performance flow diagram of such a system is shown in figure 6. In the diagram measuring values are recorded which compressors normally achieve. The condensation temperature \( T_C = 50°C \) and the evaporation temperature \( T_0 = 30°C \) correspond to the common freezer values. The operation time of the compressor is assumed to be 100%, or continuous duty. \( Q_N \) represents the electrical energy increase with a 100% rating. The cooling obtained \( Q_C, 92\% \), is slightly higher in comparison with the value shown in figure 4. This is because the thermic relationships have changed in the cooling circuit. Of the condensation heat \( Q_C \), which theoretically should be 192%, about 20% is lost due to transmission and radiation in the compressor. For water heating purposes \( Q_Y = 172\% \) is available. The heat produced is therefore 1.72 times greater than the electrical energy intake.

The excessive heat is drawn from the outside air into the room the freezer is in. As it extracts heat from its surroundings, it provides an ideal means to cool cellars. An added advantage is that no extra energy need be used. To avoid thermic feedback between the freezer and the boiler, these should not, of course, be placed in the same room.

**Economic considerations**

The condenser spiral, insulated compressor with oil cooler including various
control and safety installations will cost about £125. If we suppose that approximately £40 a year may be saved once the system is in operation, it will have paid for itself within 2½ years. Compared with other boiler systems, this is extremely short. Even when water is oil heated equivalent energy costs may be saved.

Figure 7 shows the entire system as it is available today. Twenty such systems have been tested and are still functioning well after more than eighteen months. Figure 8 gives an idea of the machinery involved in a freezer. Clearly visible are the two insulated tubes along which the cooling medium transports the condenser and oil cooler heat. In the return tubes the fluid is at room temperature and does not therefore require insulating. The four tubes are the only connections between the freezer and the boiler. A boiler is shown with a 290 litre capacity, as installed in a modern home. (Recommended for a family of four.)

Technical layout
Whereas the freezer needs to be cooled continuously, how much hot water is consumed depends on the time of day and even on the day of the week (figure 9). In the upper half of the diagram the amount of hot water consumed is shown with relation to the time of day. Also recorded are the hourly averages of consumption in a family of four on an ordinary weekday. There is a considerable difference in quantity consumed at the weekend. In a simplified manner, the total water consumption is shown in the lower half of the drawing. This is with relation to the days of the week, where the consumption and boiler efficiency ratio is expressed in kilowatt hours, in other words, as energy required by the boiler. This graph also shows the energy which a 300 litre freezer can produce for water boiling purposes. With a power rating of 1.72, barely 4 kWh or about 46% of the total energy required may be used to boil water. The boiler capacity, 290 litres, is shown to be in proportion to the cooling of the 300 litre freezer. The additional heating apparatus is exclusively used to cover the energy consumption peaks.

Otto Koehn, at the 15th AEG-Telefunken technical press colloquium

Figure 9. Specific hot water requirements for a family of four. Temperature obtained; 60°C.
No less than three microprocessor systems have been published to date by Elektor. To the beginner this may seem rather confusing. It is hoped that the following description will serve as a guide to anyone wishing to construct an Elektor system.

The systems were published in the following chronological order: the SC/MP, the games computer and the Junior Computer (JC). Although the purpose of this article is to provide a general survey rather than a detailed discussion of the manifold possibilities of microprocessors, practical examples will be given by way of illustration. (This does not imply, however, that no other uses may be found for the systems.)

First of all, let us deal with the SC/MP (pronounced 'scamp') system. Its principal feature is its modular construction. The microprocessor of the same name is manufactured by National (type number INS9060). This involves a number of printed circuit boards of the Eurocard format (approximately 10 x 16 cm) which are interconnected by means of a bus system. The bus is nothing more than a set of conductors connecting all the 1 points, the 2 points, etc. Its modular construction allows it to be a highly flexible unit. Its smallest version is made up of only two cards. The system may be extended by adding more cards to the bus printed circuit board. These will not only provide more memory capacity (additional RAM and/or ROM), but a printer, for instance, may also be installed.

The Junior Computer is constructed on a single printed circuit board (excluding the supply). An attempt has been made to build the cheapest and smallest unit possible, without eliminating any of its 'real' microprocessor characteristics. By means of a connector on the board, the Junior Computer can be coupled to the SC/MP system. The result is a SC/MP system using an additional processor.

The odd man out of the threesome is the games computer. It was designed to generate colour TV pictures directly on the screen. The pictures are programmed to move and change in form and colour. Thus, it is in fact a luxury TV game device. Additional games may be introduced (such as space war, football and Master Mind). The hardware (the computer itself) has been specially adapted: It consists of two individual keyboards of 12 keys and of a 4 key section to be used by both players. In addition, there is an input for two joysticks ('steering levers') and a loudspeaker has been...

Figure 1. The system structure of the games computer.
built in for special sound effects. Programmes may be easily changed with the aid of a cassette recorder which tapes them, so that they may be played whenever required.

The games computer was not designed for expansion since its prime purpose was for programming. Any possible future additions will only affect its memory capacity. Both the Junior Computer and the SC/MP were designed for more general use. Not only do they carry out specific tasks, but games may also be played (without a TV). Both machines are capable of developing programmes (already included in the standard monitor programme) and of operating in high-level computer languages. The SC/MP, for instance, can use tiny BASIC.

Every command to be carried out by the SC/MP is then issued by a terminal. This is a separate unit which has a keyboard and VDU and/or printer. The keyboard consists of figures 0...9 as well as the alphabet and specific control characters. In order to operate this system effectively, therefore, a terminal is essential because it enables the computer to communicate in a high level language. Since 'normal' words are used, the alphanumeric keyboard is necessary.

The Elekterminal was described in Elektor's November and December 1978 issues. Instead of the Elekterminal a hexadecimal keyboard and display may be used. This is a separate module described in the earlier series on the SC/MP. The system is then fully operational at a machine language level, and the Elekterminal can always be added at a later date. Without extensions, the JC is also programmed on machine language.

The microprocessor

The first aspect to consider is which microprocessor should be selected. The microprocessor is at the heart of any microcomputer system, and to a great extent it determines its capabilities and the speed at which tasks are carried out. At first sight the best choice would appear to be a microprocessor with great potential and high speed. On the other hand, it is very difficult to programme hundreds of instructions. Experience has shown that, ideally speaking, the programmer should know them all by heart. As far as speed is concerned, it is of course an advantage for the processor to be fast, without needing a higher speed (and therefore higher priced) memory. In practice, however, programs tend to be held up only when high level languages are used or when complicated mathematical calculations are made.

Another aspect which merits attention is how many programmes are available. Generally speaking, a processor may take over other programmes, after minor modification, provided these were written for the same type of processor. In this respect, the 6502 is a good choice.

Each system hitherto discussed relies on a different microprocessor: the games computer on the 2650 from Signetics, the SC/MP on the INS 8060 from National and the Junior Computer on the 6502 from Rockwell. Of these, the SC/MP (8060) operates in the simplest and slowest manner. The 6502, on the other hand, is the most complex and fastest. Between the two extremes lies the 2650's performance. Since the SC/MP's relatively slow operation is sometimes considered a handicap, a processor card has been manufactured for it which is obtainable together with a faster Z-80 processor (not by Elektor). By way of conclusion, a brief description of the construction of each system will be given and especially with regard to their combination possibilities. For further technical details, reference should be made to articles on the subject published in Elektor.

The TV Games Computer

Number one on our list is the games computer. It consists of a central printed circuit board, including a keyboard (see figure 1), a power supply and, usually, a UHF modulator so that a normal colour TV with an aerial input may be used. In addition, it is advisable to make use of a cassette recorder to store the programmes. To facilitate programming, the monitor is equipped with extensive debugging capabilities including two breakpoints. Two joy sticks may also be added to control the games.

The Junior Computer

For simplicity, the Junior Computer

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Figure 2. The section to the left of the dotted line contains the basic Junior Computer. The modules to the right may be added if required.
Figure 3. The SC/MP has been equipped with a terminal. By placing a pre-programmed ROM with Tiny BASIC on the processor circuit board, it may be operated in BASIC. Expansion possibilities are shown to the right of the dotted line.

is built on a single printed circuit board (see figure 2). Of course, it also needs a supply capable of producing +12, +5 and -5 V. The keyboard is constructed directly onto the board. The six seven-segment displays are mounted onto a small auxiliary board which is soldered in a slanting position onto the central board. Later on, a cassette interface and one or two cassette recorders may also be added. Furthermore, a connection may be made to the SC/MP bus by means of the connector which is on the board. This can come in handy, for instance, when more memory is required than is included in the circuit (1 K EPROM with the monitor program and 1 K RAM).

The Junior Computer system operates in a hexdecimal code; in other words, using 0. . .9 and then A, B, C, D, E, F. The monitor program features a hex assembler. When jump instructions are encountered, the hex assembler provides the correct byte for the corresponding location. The monitor then passes on the addresses to the computer. Before more complicated tasks (using high level languages, assemblers, etc.) can be fulfilled, a terminal must be connected. This can be done with the aid of the cassette interface board. For the necessary memory power, however, more EPROM's will have to be introduced. This involves using the 8 K EPROM + 8 K RAM card belonging to the SC/MP. For taping purposes, the cassette interface board will have to be added. Additional EPROM and/or RAM will have to be included, whenever one wishes to operate the available editor assembler, disassembler or when programming in high level languages (such as BASIC).

The SC/MP system

Finally, it is time to consider the SC/MP system. Because of its modular construction, several configurations are possible. The minimum (BASIC version) system is based on two cards (see figure 3). The first is the processor card which includes an address and data buffer and the possibility to connect a terminal (RS 232 interface). For the second the 8 K EPROM + 8 K RAM can be chosen. If so, the monitor program must be part of the EPROM and as much RAM as required (from 1 K to 8 K) may be added. When a keyboard is used with the 2 card system it is able to run BASIC programs. The processor card has an IC (ROM) socket specifically for this.

For programme storage purposes the cassette interface may be added. With the aid of the matrix printer card a printed listing of machine language programmes may be obtained. The supply voltages required depend on which EPROMs are used. The 8 K EPROM + 8 K RAM card employs 2716 EPROMs and requires 5 V. On the cassette interface card there is room for EPROMs of the 5204 type and these require ±5 and -12 V. Instead of the 2716, the 2708 may also be used. As a result, two more supply voltages (+12 and -5 V) will have to be added to the 8 K circuit and the memory capacity will be halved (to 4 K). The existing supply already produces the ±5 V and -12 V. On the SC/MP bus lines, all voltages mentioned are available. Apart from the modules mentioned above, there are still a few cards available which are based on a somewhat smaller system which communicates with the outside world by means of a keyboard, and eight seven-segment displays. This model resembles the Junior Computer in its elementary form. With the aid of these cards, a unit may be assembled as follows (figure 4).

The SC/MP processor card plus the extension card constitute the actual computer. The data lines are not buffered, thereby restricting the size of the system. In order to build up a more complex system, a data bus buffer must be added (printed circuit board number 9972). The operator has 1½ K of EPROM available (the monitor programme) and 1 K of RAM. The RAM may be expanded with the aid of the 4 K RAM card. Data is written and read by means of 26 keys and eight seven-segment displays, installed in the hex-I/O printed circuit board. An auxiliary board will be needed for the connection of a cassette recorder (number 9905). A terminal, however, may be fitted with another circuit board (78101, interface for microprocessor). The layout is shown in figure 5.

Which one is for you?

These then are the Elektor computers to date. Three complete systems designed for different purposes. The SC/MP for constructors with a desire for a
Figure 4. The smaller SC/MP system (using different cards)

A large system which can be expanded and/or modified as and when required. The TV Games Computer for those who want to see the results of their programming instantly in a visual form. The Junior Computer for the beginner, easy to construct and economical with great potential.

- For the beginner... the obvious choice will be the Junior Computer with its excellent teaching facilities.
- For the expert... the SC/MP will probably be the most desirable with its many system variation possibilities. Why not add the Junior Computer to it (have a two computer family)?
- For the programmer... The TV Games is 100% FUN. Designed specifically for programming, it succeeds in its purpose very well.
- For the constructor... If building is your wont the SC/MP will be fine. You can fill two kitchen tables with it.
- For the experimenter... We are all one of these at heart really. And if you fit all of the above categories there can only be one answer...

It is worth noting here that two new computer books will be available from Elektor, one for the SC/MP and one for the Junior Computer.

What does the future hold?

There are two things you can be sure of with Elektor, we always have something up our electronic sleeve. How about a PASCAL compiler for the Junior Computer or a complete computer cassette system? There are even rumours of a new VDU system using an unconverted TV set. Maybe even a new computer... who can tell? Just watch this space!

Figure 5. A terminal may also be built with the aid of Elektor printed circuit boards. 16 rows of 64 characters may be displayed onto an ordinary TV screen.
There are many readers who would like to know more about home computers but who may not be technically minded or who consider them too complicated to understand. These two reasons, coupled with cost, tend to prevent many people from taking the plunge. To help overcome these problems we have designed the Junior Computer (JC). Do not be misled by the term 'Junior'—this computer provides the first step to understanding large and powerful systems. Although small in size, the Junior Computer can be used with high level languages (PASCAL for instance). This is possible because it uses a simplified method of operation and has the advantage of various expansion possibilities.

**junior computer**

The cost and complexity of home computers is a serious deterrent to the newcomer to computer operating and programming. We know of many readers who would like to build their own but who lack the necessary technical knowledge. The Junior Computer has been designed (for just this reason) as an attempt to open the door to those readers who need a push in the right direction.

It should be emphasized that, although simple to construct, the Junior Computer is not a 'toy' but a fully workable computer system with the capability for future expansion. It has been designed for use by amateurs or experts, and software to be published will include a PASCAL compiler—the computer language of the future.

The purpose of this article is to give a general description of the operation and construction of the Junior Computer. It has been decided to publish a more detailed description in book form. The arrival of 'The Junior Computer' Books 1 and 2 on the market will be announced shortly. This, however, is a preview intended to give the reader an idea of what the computer entails.

The heart of the JC occupies no more than a single printed circuit board which should dispel any fears produced by large and complicated systems. The intention of this article is to encourage readers to take the initial steps towards constructing and operating their own personal computer. Extensive and precise details will not be dealt with here but will be published in depth in book form—the Junior Computer Books 1 and 2. We can however whet the appetite and set the ball rolling. Specific data concerning the computer are given in Table 1, this is intended for readers who are already familiar with computers.

**Block diagram**

The fundamental features of the Junior Computer are shown in the simplified block diagram of figure 1. The heart of any computer system is the CPU, or central processing unit. In this particular case it is a 6502 microprocessor, a 40 pin chip that you can hold in the palm of your hand—but shouldn't! Its purpose is to control communications between the various units inside the computer in accordance with the instructions of the program. A clock generator (oscillator) serves as a 'pacemaker' for the processor.

A certain amount of memory is required by the microprocessor to store programs and data. In the JC it consists of two sections. The first one for storing permanent data and the monitor program. The monitor program contains a number of routines which perform such chores as program loading,
debugging and general housekeeping. The second section of memory is used for storing temporary data and program instructions.

The block marked I/O (input/output) maintains contact between the computer and the outside world including the keyboard and display. In the circuit the I/O appears as the PIA, or peripheral interface adapter. It takes care of the data transfer in two directions and can (temporarily) store data. The operator communicates with the computer via the keyboard and display.

Computers are not as 'intelligent' as some television programmes would have us believe. In fact, they merely carry out (programmed) instructions in a certain (programmable) order. There are three sets of parallel interconnections (called buses - not the Midland Red type!) which carry the various data and control signals. First of all there is the data bus to consider. It is made up of a number of lines along which data travels from block to block. The processor must also be able to indicate the memory location where data is to be stored or removed. This is performed by the second bus, the address bus. Last, but by no means least, is the control bus which ensures that the CPU is able to control the internal status, for instance the nature and direction of data transfer and the progress of successive program sections.

This then very briefly covers the various blocks, their functions and their interconnections. We can now move on to look at the circuit in greater detail.

**Circuit diagram**

The circuit diagram of the entire Junior Computer (except for the power supply) is shown in figure 2. Now that the block diagram has been examined, each section should be easily recognisable. The 6502 microprocessor is IC1. Below it is the clock generator formed by N1, R1, D1, C1 and the 1 MHz crystal. The system uses a two-phase clock, shown in the circuit diagram as signals Ø1 and Ø2. The memory is constituted by IC2, IC4, IC5 and part of IC3. The monitor program is stored in IC2, a 1024 byte EPROM (Erasable Programmable Read-Only-Memory). This is the basic program in the computer (not to be confused with BASIC - a high level computer language). The RAMs (Random Access Memory) IC4 and IC5 serve as user memory and together have a capacity of 1024 bytes.

In the PIA, IC3, there are another 128 bytes of RAM. The PIA constitutes a data buffer which controls all the data transfer passing in either direction between the computer and ports A and B. The port lines are fed out to a 31 pole connector. IC3 also contains a programmable interval timer. The displays (Dp1 ... Dp6) and keys...
Figure 2. The circuit diagram of the Junior Computer.
(S1 ... S23) are at the bottom of the circuit diagram. Of these, sixteen are for the purpose of entering data and addresses in hexadecimal form and the remaining seven have various control functions. Data to the displays and from the keyboard is transferred across seven lines from port A. The information on the displays is controlled by the software in the monitor program, which also ensures that key function signals are recognized. IC7 multiplexes the displays and periodically checks the state of the rows of keys to see which, if any, is being depressed. With the aid of switch S24 the display may be switched off.

The displays may be used in two different ways. Usually, the four left hand displays will indicate an address and the two right hand ones will indicate the data in the address location concerned. As a second possibility, the two left hand displays can show the (hexadecimal) code of an instruction while the others display the address of the data corresponding to this instruction. This makes program entry much easier.

The address decoder, IC6, provides chip select signals for each of the various memory blocks. These appear as K7, K6 and K5 for the EPROM, PIA and the RAMs respectively. The other five selection signals are available externally for memory expansion. The RAMs also require a R/W (read/write) signal. This is made available via gate N6 and is generated by a combination of the R/W signal in the 6502 and the 02 clock pulse (02 = data bus enable). Another control signal is the reset signal RES, which places the microprocessor and the PIA in the correct initial condition for the monitor program. A reset is generated when key RST (S1) is pressed and half of a 556 timer (IC8) is used to suppress any contact bounce this key might produce.

There are two ways in which a program being run can be interrupted by means of the NM1 (non-maskable interrupt). The first one is provided by the STOP key S2 (which uses the other half of IC8 for contact bounce suppression) and the second is provided by the STEP switch S24 when this is in the 'ON' position. When the output of N5 then changes from high to low, the IRQ (interrupt request) connection causes the program being run to be interrupted, for instance by programming the interval timer in IC3. Also present on the control bus are the two clock signals O1 and O2 which control the PIA and the RAM R/W signals. These determine the direction of data transfer. Finally, lines RDY, SO and EX provide possibilities for future expansion.

All the address, data and control signals are fed to a 64 pole expansion connector which, as its name suggests, is meant for the purpose of expanding the system further at a later stage. Figure 3 shows the power supply for the Junior Computer. This produces three voltages: +5 V for all the ICs and the displays, +12 V and −5 V for the EPROM (IC2). Capacitors C5 ... C14 ensure the necessary decoupling.

A few remarks

Before work is begun on the construction of the Junior Computer, two more aspects have yet to be considered. The entire system is built up on three printed circuit boards of which one is double sided with plated through holes. It is advisable to check all the through connections with an ohmmeter to make sure that both sides of the circuit are well connected. This will avoid problems later, for after soldering it is very difficult to trace any breaks.

Normally, of course, the 2709 EPROM will not have been programmed when it is bought. The monitor program (or 'hex dump') is given, so that the reader who has a PROM programmer at his disposal may program the IC himself. Alternatively, pre-programmed 2708s can be purchased from the retailers listed at the end of this article.

How to build the Junior Computer

Construction of the Junior Computer is not difficult by any standards. If it is assembled carefully (paying particular attention to solder connections) and the instructions are followed to the letter, very little can go wrong. The three sections of the JC are each constructed on a separate printed circuit board: the main board (including keyboard) the display board and the power supply.

The smallest of the printed circuit boards is the display board (figure 6). This is connected to the main board by means of thirteen wire links. The seven-segment displays can be soldered directly onto the printed circuit board. The main board is double sided and is shown in figures 4 and 5. With the aid of the component overlay it is possible to see on which side to mount the various components. First, resistors R1 ... R20 and diode D1 are mounted, then capacitors C1 ... C13, followed by all the IC sockets. It is advisable to use IC sockets especially for IC1 ... IC3. Be sure to use a top quality type with gold contacts.

The other side of the board can now be assembled. Switches S1 ... S23 (Digitast) and LED D2 (remember the LEDs polarity) can now be mounted. Two holes remain free next to the keyboard for switches S24 and S25. These switches are connected to the main printed circuit board using short lengths of insulated wire. A single wire link is placed on the main board to connect the 'D' input of IC6 to the zero volt rail. The other connection indicated between D and EX is meant for future expansion. The 31 pole connector is mounted on the keyboard side, followed by the 64 pole connector which is positioned on the other side of the board.

The display printed circuit board can now be connected to the main board. The distance between the two boards
should be about 5 mm. All that remains to complete the computer board is to solder the 1 MHz crystal in place, and finally, fit IC1 . . . IC3 (the expensive ones) into their sockets. The main board is now complete.

The power supply has been left until last. The simple construction should not give anyone any headaches. All components are mounted according to figure 7, not forgetting the mica insulating plate (with a smear of heat-sink compound) under IC2. Connections between the power supply and the computer can be made using a four wire cable to the 64 pole connector as follows:

+12 V to pin 17c
+5 V to pins 1a, 1c
−5 V to pin 18a
0 V to pins 4a, 4c

It would be wise to make absolutely sure that these connections are correct. An error here can be very costly.

This completes the construction of the Junior Computer and now we approach the moment of truth.

Switch on

Just before you do that, one more check-over would not be a waste of time. Are all the chips the right way round? Are there any cut offs of wire lying on the boards? A final thorough inspection could save you money. Now switch on . . . and of course nothing happens, the display remains until. There is no reason for alarm yet, everything is exactly as it should be. Now press the RST key and random hexadecimal characters appear on the display. This is quite in order and as good a proof as any at this time that your JC is functioning correctly. It can now be fitted into the case of your choice.

Something wrong, after all?

Unfortunately, (due to Murphy's Law no doubt) there is a possibility that pressing the RST key will depress the operator rather than cause anything to appear on the displays. This will of course occur with an unprogrammed 2708 (IC2). A survey of the most common errors and how to deal with them are given below.

First verify that the supply voltages at the 64 pole connector are as follows:

Figure 4. Component overlay of keyboard (a) and components (b) of the main printed circuit board (EPS 80098-1).
Parts list junior computer

Resistors:
R1 = 100 k
R2, R3, R4, R14, R15, R16 = 3 k
R6 = 4 k
R6 = 330 Ω
R7, R13 = 68 Ω
R17, R19 = 2 k
R18, R20 = 68 k

Capacitors:
C1 = 10 μ ceramic
C2 = 47 μ/8 V tantalum
C3, C4 = 100 n MKH
C5, C14 = 1 μ/35 V tantalum

Semiconductors:
IC1 = 6502 (Rockwell)
IC2 = 2708
IC3 = 6632 (Rockwell)
IC4, IC5 = 2114
IC6, IC7 = 74145
IC8 = 556
IC9 = 74LS00, 7400, 74LS132
IC10 = 74LS01, 7401
IC11 = ULN2003 (Sprague)
D1 = 1N4148

Miscellaneous:
S1, S2, S23 = digitast (Shadow)
S22 = digitast + LED
S24 = double pole switch
S25 = single pole switch
Dp1, Dp6 = MAN 4640A
perpendicular to DIN 41612
connector female to DIN 41617
1 Hz crystal
1 24-pin IC socket
2 40-pin IC sockets

Parts list supply

Capacitors:
C1, C2, C10 = 470 μ/25 V
C3, C11 = 47 μ/25 V
C4, C5, C6, C9, C12,
C13 = 100 n MKH
C6 = 2200 μ/25 V
C7 = 100 μ/25 V

Semiconductors:
IC1 = 78L12ACP (5%)
IC2 = LM 309K
IC3 = 79L06ACP (5%)
D1, D6 = 1N4004

Miscellaneous:
T1 = transformer prim. 220 V
sec. 2 x 9 . . . 10 V/1,2 . . . 2 A
S1 = double pole switch
F1 = fuse 500 mA, with fuse holder
Table 1

General information on the Junior Computer
- single board computer
- programmable in machine language
  (hexadecimal)
- microprocessor type 6502
- 1 MHz crystal
- 1024 bytes of monitor in EPROM
- 1024 bytes of RAM
- PIA type 6532 with two I/O ports,
  128 bytes of RAM and a programmable
  interval timer
- six digit seven segment display
- hexadecimal keyboard with 23 keys:
  16 'alpha' keys and 7 double function
  control keys

Control keys (normal mode)
+ : increment address on display by
  one
DA : enter data
AD : enter address
PC : call up contents of current program
     counter position
GO : start program from address on
     display
ST : interrupt program by way of NMI
RST : call up monitor
STEP : step by step run through program

Control keys (editor mode via ST)
insert : insert program step before address
   shown on display
input : insert program step after address
   shown on display
skip : jump to next opcode
search : search for a certain label
delete : delete row of characters on display

Possibilities
debugging : all internal registers may be
   shown on display
hex editor : label identification with
   hexadecimal figures JMP,
   JSR, branch instructions
   operate with label
hex assembler : conversion of label numbers
   into displacement values for
   real address
branch : calculate address offset for
    jump instructions

Applications
- compatible with SC/MP bus
- can be used as a basis for many expansions
- can be used as a 6502 CPU card
- educational computer for beginners
- can be expanded with:
  eletkernale
  cassette interface
  video interface
  BASIC and PASCAL
  matrix printer
  assembler
  disassembler
  editor

Figure 5. The track pattern for both sides (a and b) of the main board.
Figure 6. The display printed circuit board (EPS 80089-2).

- between pins 1a and 4a: +5 V ± 5%
- between pins 17c and 4a: +12 V ± 5%
- between pins 18a and 4a: −5 V ± 5%

If one of the voltages measured is not within the above tolerance, connections between the supply and computer should be removed and the supply checked separately.

If the supply voltages are in order, but the computer refuses to react to the RST key, further measurements will have to be carried out. The voltage between pin 13 and pin 17 of IC8 should be less than 0.5 V when RST is
pressed. If this is not the case, the error will be in:
- the timer IC8
- the pull-up resistor R2
- the RST key S1.

With the supply switched off, the resistance between pin 12 of IC6 and 0 V (connector pin 4a) can be measured. If there is no 'short' between these two points, the wire link will have been placed in the wrong position on the main board.

The last check to carry out involves the clock generator and for this an oscilloscope will be required. The CPU produces two clock signals which are fed to the expansion connector: 01 on pin 30a and 02 on pin 27a. With the aid of the 'scope it can be seen whether a 1 MHz square wave is present at both points (minimum RMS value 3 V). In the event of the oscillator not operating or showing a defect, this will probably be due to capacitor C1, diode D1 or IC9.

Of course, other faults are possible, but the above checks should clear most problems.

For readers who have facilities for programming their own EPROM (IC2) the monitor dump is given here (figure 8). There are 64 rows of 16 bytes each, a total of 1024 bytes. The first column gives the hexadecimal address for the byte in col 8.

Your Junior Computer is now rearing to go and it it possible to begin your programming lessons. Each section of the Junior Computer Book is clearly illustrated with examples that can be put into practice on your very own computer. As mentioned earlier, there are plans afoot for the publication of a number of programs and a PASCAL compiler for the JC. Look out for further details.

Figure 7. The printed circuit board and layout of the power supply (EPS 80089-3).
**Pest Pester**

Perpetually plagued by mosquitos? This summer, protect your person with the 'Pest Pester'.

**Disco Lights**

Brighten up your parties to the beat of the disco greats. Not Travolta, but the next best thing – Disco Lights.

**8K RAM + 8K EPROM**

SC/MP and Junior Computer owners who are short on memory may extend their systems with an 8K RAM + 8K EPROM board.

As usual the July/August issue will have more than 100 circuits to keep you busy over the summer.
Even though the superheterodyne receiver is an invention dating from the earliest days of radio, the principle behind it nevertheless remains valid. Almost every radio operates on an oscillator which is mixed with the aerial signal to produce an IF signal. Early tuners had terrible drifting problems. In modern units however, the oscillator gets a little electronic help. This is necessary because it is by no means easy to continue oscillating for long periods without the penalty of some drift. This help may come in the form of a circuit which subjects the differential output of the mixer to an electronic check. It must continually ask itself: is the IF frequency right? If a deviation frequency divider is engaged in the operation.

One important drawback of the frequency synthesiser is the oscillator’s inability to generate any given frequency (within its range); it can only produce those which are equal to a fixed frequency, multiplied by a whole number. Thus, a frequency synthesiser cannot be tuned continuously — its output jumps step by step. That is the price that has to be paid for stability! A price we can well afford, as long as we make sure the steps are small. Furthermore, the signals to be received are not just anywhere on the frequency band: for broadcast transmitters, the band is also divided into fixed steps. The constant comparison made between the oscillator frequency and a stable crystal frequency also takes place in the frequency lock system described in this article. The block diagram in figure 1 demonstrates the basic principle.

The input signal of the frequency lock is generated by the oscillator in the receiver’s tuning section (HF): fosc. The frequency lock system provides the oscillator with an output signal in the form of a control voltage UC. The circuit controls UC in such a way that fosc is exactly equal to one of a series of ‘scanning frequencies’, frequencies which are separated by a fixed distance.

**How it works**

The heart of the frequency lock circuit is the D flipflop FF. This operates as a harmonic mixer with two inputs; the D input and the clock input of the flipflop. The input signals are symmetrical square waves, at frequencies fosc and fc respectively. A symmetrical square wave also appears at the Q output of the flipflop with a frequency fq. The latter frequency is, of course, dependent on the two input frequencies, according to the following formulæ:

\[ f_q = \left| f_{osc} - c \cdot f_c \right| \text{ and } f_q \leq \frac{1}{2} f_c \]

The two vertical lines refer to the absolute value. In other words, the numbers not preceded by a plus or a minus sign (otherwise \( f_q \) would turn into a negative frequency, which would be ludicrous). The figure c in the formula is a positive whole number. Let us suppose fosc equals 2005 kHz and fc equals 20 kHz. From the condition \( f_q \leq \frac{1}{2} f_c \) it then follows that c must be equal to 100 and \( f_q \) will be 5 kHz. If fosc is equal to 2010 kHz, c has two possible values, namely c = 100 and c = 101 (\( f_q \) will then be 10 kHz). Therefore, a raster of \( \frac{1}{2} f_c \) (the clock frequency) is created.
Variable c is also called the harmonic number. If the input signals of the harmonic mixer, $f_{osc}$ and $f_{cl}$, are kept constant, the formula gives the value of the output frequency $f_{q}$. However, there is another way of getting the same result: by keeping the output frequency together with one of the input frequencies at a constant level. Let us suppose that $f_{q}$ is equal to 250 Hz and $f_{cl}$ is equal to 1000 Hz. Which values may $f_{osc}$ assume without invalidating the formula? Let us start by substituting a 1 for c. Then:

$$250 = |f_{osc} - 1 \cdot 1000|$$

That is correct where $f_{osc} = 1250$ Hz (and remember we’re talking about an absolute value) where $f_{osc} = 750$ Hz. Now let us replace c with a 2. The formula then reads:

$$250 = |f_{osc} - 2 \cdot 1000|$$

and that is correct where $f_{osc} = 2250$ Hz and $f_{osc} = 1750$ Hz. Any whole number (“integer”) may replace c, with the result that $f_{osc}$ may assume the following values: 750 Hz, 1250 Hz, 1750 Hz, 2250 Hz, 2750 Hz, 3250 Hz … etc. Exactly the series of raster frequencies required!

The question is now: how can we maintain a constant single input ($f_{cl}$) and output frequency ($f_{q}$)? The input frequency should not give any problems, for it may be derived from a stable crystal oscillator. In the block diagram $f_{cl}$ is shown to originate by dividing the frequency of a crystal oscillator by n. Keeping the output frequency $f_{q}$ level is an easy task, for it is impossible to influence it directly. The only other frequency which can be directly affected is $f_{osc}$. That is why an automatic control system is used. $f_{osc}$ is controlled in such a way that $f_{q}$ remains constant. For this purpose $f_{q}$ is continuously compared to a stable reference frequency $f_{r}$. Both signals are fed to simple pulse generators, one of which produces a positive pulse at every period, the other a negative pulse. The output signals of the two pulse generators are added together and summed by an op-amp IC. The output signal of the mixer/buffer produces the control voltage $U_c$ which regulates the frequency $f_{osc}$.

If $f_{q}$ is equal to $f_{r}$, the average output voltage of the counter (IC2) will be nil and so will the control voltage $U_c$. At the input of the mixer there will be as many positive as negative pulses. If $f_{q}$ is too high for one reason or another, more negative than positive pulses will reach the input of the mixer. After some time the output of the mixer will also become negative. This will cause $U_c$...

---

**Figure 1. A block diagram of the frequency regulator. An important part is played by the D flipflop FF, which operates here as a harmonic mixer. The frequency regulator continually compares the oscillator frequency with a highly stable crystal frequency.**

---

**Figure 2. In the elaborated layout the structure of the block diagram is still noticeable.**
Figure 3. This window comparator makes the control voltage visible. When the green 'lock' LED lights, the control circuit is activated.

Figure 4. This circuit may be added to existing HF oscillators. Diode D9 is in series with C9, which is in parallel with the tuning capacitor.

In practice
Now that the block diagram has been dealt with in detail, few words need be said on the practical layout given in figure 2. In any case, the block diagram can easily be recognized in it. One highly advantageous aspect comes to light immediately: in spite of the circuit's complicated operation, it is very reasonable in price. Such a remarkable piece of electronic ingenuity requires only a few IC's and one or two components. It is much simpler than the PLL system.

IC1 contains a crystal oscillator and a fourteen bit binary counter. For the crystal, a 4.43 MHz type has been chosen, like the one used in colour TVs. It is inexpensive and easily obtainable. It can be replaced by a crystal of a different frequency without any difficulty, provided that it is between 1 and 6 MHz. Only the difference in individual distance between rater frequencies will show that a crystal of another frequency is being used.

The signal at pin 3 if IC1 has a frequency of approximately 270 Hz. This is the input signal of a second counter, IC2. There the frequency is divided once more by four (output Q2 on pin 11), so that a signal of some 70 Hz is generated. This is indicated as signal fosc in the block diagram. Thus, the raster frequencies come to be separated by approximately 70 Hz. A signal is also derived from the Q3 output of IC2. Its frequency is divided by two by means of FF1 so that a frequency regulation of approximately 17 Hz is achieved.

FF2 is the harmonic mixer. The two pulse generators are each simply constructed from a diode and a resistor (D1/R2 and D2/R3 respectively). To avoid having to use a split supply they both operate at one half of the supply voltage. Op-amp IC4 is wired as a mixer and operates as the one given in the diagram together with the inverting buffer amplifier. Its output voltage is the control voltage UC. The 'reset' push button S has been provided to interrupt the entire control process. This is important when tuning to another frequency.

It is absolutely essential for IC4 to be the type indicated, because it has a high impedance FET input.

Indicator
The control voltage UC is not only used to regulate the frequency of the oscillator in the HF section, it is also desirable to make its value apparent in one way or another. The ring circuit of the frequency regulator is only operative when UC does not deviate too much from its zero level and only then is the receiver optimally tuned. For this reason UC is made visible with the aid of the indicator circuit of figure 3. This is called a window comparator. When UC is 'in range', the green 'lock' LED (D6) lights. The receiver is then tuned. If one of the two red 'out of range' LEDs lights, it is advisable to alter the receiver's tuning to prevent the frequency regulator from becoming inactive (when D4 lights it moves to a lower frequency, when D7 lights, it moves to a higher one).

Connecting it to the HF oscillator
There are many different receivers and as many different high frequency oscillators. There are, therefore, various ways to connect the frequency regulator.

The classic model operates with the aid of a resonance circuit consisting of a coil and a variable capacitor in parallel. The oscillator signal is across the resonance circuit. The oscillator frequency may be regulated by the control voltage if a varicap diode is wired parallel to the tuning capacitor (see figure 4).

The oscillator signal across the resonance circuit is derived by means of an amplifier stage with a dual gate MOSFET. Due to the high input impedance of the MOSFET, the resonance circuit is lightly loaded. The amplifier stage has two outputs: one for the fosc signal sent to the frequency regulator and one with the same signal to feed the input of.
a frequency counter, which may be included. The latter output is of no importance to the frequency regulator, but is included, as an option.

D9 is the varicap diode. It is connected to the tuning capacitor in series with C9. Otherwise, the slightest variation in Uc would lead to an enormous change in the oscillator frequency and result in instability.

If the tuning loops of the receiver are already provided with varicaps, another one will, of course, not be necessary. The control voltage Uc may then be added to the tuning voltage at the varicap of the oscillator circuit. D9, C9 and R22 (figure 4) can then be omitted.

If, as more often than not is the case, the receiver is provided with a 'counter' output (at which the oscillator frequency is available), figure 4 may be left out altogether. The input of the frequency regulator is then connected to the 'counter' output.

Figure 4's circuit must be attached as closely as possible to the oscillator. It is, as it were, part of the oscillator. The input of the amplifier stage is highly sensitive and prone to interference.

Experimenting

A circuit like the frequency lock is ideal for hobbyists who enjoy experimenting. The layout offering the best results differs from case to case. It depends on the drift features of the receiver oscillator and in some cases it may be worthwhile to change the original layout a little.

For instance, the clock frequencies of FF1 and FF2 may be chosen at a higher or lower level by deriving them from IC2 outputs other than the Q2 and Q3 now used. A prerequisite is, however, that the two IC2 outputs used be next to each other (the clock frequency of FF1, therefore, is half that of FF2). Choosing a higher or lower level clock frequency will affect the distance between the raster frequencies (they will be either closer to or further away from each other) and also the speed at which the frequency regulator operates.

If the clock frequencies of the flipflops change, capacitors C3, C4 and C5 will also have to change. At twice the frequencies they will be half their value. In stead of enlarging C5 (for which an electrolytic capacitor may not be used), R4 and R5 may have a higher value. The frequency regulator may also use an external reference frequency. This could, for instance, be derived from the time base of a counter. Such a frequency (for instance 1 MHz) may be fed by means of a capacitor of 39 pF to pin 11 of IC1. In that case, R1, C1, C2 and the crystal are no longer required. If a low frequency reference AG is available (of say, 100 or 250 Hz), IC1 may even be omitted entirely. The reference frequency can then be directly connected to pin 1 of IC2. Any external reference frequency must, of course, be (crystal) stabilised.
The subject of digital audio has been dealt with in Elektor before and certainly will be again. The benefits are impressive, so much so that virtually all major manufacturers of audio equipment are investigating its possibilities. Recording companies too are aware of the potential of a digital system (digitally recorded records are already available commercially).

Until quite recently, the performance of PWM amplifiers was disappointing due to the poor quality of transistors used. With the introduction of modern high speed switching transistors, PWM is now coming of age.

PWM amplifier

In spite of some initial teething troubles, Pulse Width Modulation (PWM) is considered by many to be the next step in audio circuit design. The PWM amplifier described in the Elektor September 1979 issue has been used as a model for the following article. Although it has only a modest 3 watt output, it is a practical and efficient amplifier.

E. Postma

The PWM amplifier

In Elektor’s December 1978 and September 1979 issues, a fair amount was said about PWM amplifiers. However, it might be a good idea to recap the principles briefly. A PWM amplifier contains a symmetrical square wave generator. The duty cycle of this square wave is then modulated by the audio signal. The output transistors do not operate linearly but function as switches, that is, they are either full on or off. Under quiescent conditions the duty cycle of the output waveform is 50% which means that each of the output transistors is fully saturated (conductor) for an equal amount of time. The average output voltage is therefore zero. It therefore follows that if one of the output switches is closed for a longer period than the other, the average output voltage will then be either negative or positive depending on the polarity of the input signal.

It can be seen then that it is the average output voltage that is proportional to the input signal. Since the output transistors function exclusively as switches, very little power loss occurs in the output stage.

The September 1979 issue discussed a variation of the above principle. This was a self oscillating PWM amplifier in which the square wave generator, the pulse width modulator and the output stage formed a single unit. This produced an efficient amplifier with only a very small number of components. A modified version of that circuit with a printed circuit board is described here.

The circuit diagram

The circuit of the complete amplifier is shown in figure 1. It can be seen that a PWM amplifier need not be very complicated at all. The input signal is fed to an op-amp IC1. This is used as a comparator and is followed by a number of schmitt triggers in parallel. This has two purposes. Firstly the wave is 'square' and secondly sufficient base drive current is needed for the output stage which uses two ordinary but fairly fast transistors (BD 137/138).

The entire amplifier oscillates and produces a square wave. This is because one of the inputs of the comparator (IC1) is connected to the output by means of an RC network. Both inputs of IC1 are biased to one half of the supply voltage using voltage divider R3/R4. Whenever

Provided Murphy doesn’t get in the way, we should have an oscillator. Now we have to pulse width modulate it. The level at the inverting input of IC1, which is used as a reference, does not remain constant but is determined by the audio signal. The point at which the output of the comparator changes, is also determined by the amplitude. As a result the width of the square waves is constantly changed (modulated) by the audio signal.

At the output of the amplifier, filtering is required: it is not supposed to act as a 700 kHz transmitter! An LC/RC network is used, consisting of L1/C6 and C7/R6.

With a load of 8 ohms and a supply voltage of 12 volts, the amplifier produced 1.6 watts. At 4 ohms, 3 watts were measured. Cooling the output transistors was not necessary. The harmonic distortion proved to be surprisingly low for such a simple design. Less than 0.32% total harmonic distortion from 20 Hz-20 kHz was measured.

Figure 2 shows the printed circuit board and parts layout for the amplifier. Its construction requires little time and money, so it offers an excellent opportunity for anyone wanting to become better acquainted with PWM.
Figure 1. The self-oscillating PWM amplifier. With a 12 V supply, it will deliver 3 watts into 4 ohms.

Figure 2. The printed circuit and parts layout of the PWM amplifier.

Parts List

Resistors:
- R1 = 22 k
- R2, R4 = 1 M
- R3, R4 = 2k2
- R5 = 470 k
- R6 = 802
- P1 = 100 log. potentiometer

Capacitors:
- C1, C2 = 100 n
- C3 = 100 p
- C4 = 100 μ/10 V
- C5 = 100 μ/16 V
- C6 = 68 n
- C7 = 470 n
- C8 = 1000 μ/10 V
- C9 = 2n2

Semiconductors:
- IC1 = CA3130
- IC2 = 40106
- T1 = BD137
- T2 = BD138

Miscellaneous:
- L1 = 39 μH
interface and software on one Eurocard

No cassette interface is included in the BASIC microcomputer described in Elektor, May 1979; furthermore, NIBL doesn't include suitable cassette routines. The obvious solution is to combine these two missing links on a single p.c. board: a handful of ICs for the interface hardware, and ½ K worth of software in EPROM.

With these extensions, programs for the BASIC microcomputer can be stored on and retrieved from tape.

The combination of cassette interface plus the necessary software on a single p.c. board offers several interesting possibilities:

- Users of a (normal) SC/MP system can use this p.c. board to keep certain special programs close at hand.
- Users of the BASIC microcomputer can use this board (without the components for the interface) for permanent storing of BASIC programs — control routines, say.
- Even without the NIBL ROM, the BASIC computer board makes a good CPU card, with complete in-and output buffering. The EPROM section can therefore be used to store a 2 K-byte monitor routine (we are working on this!), located on page 0. The BASIC card already contains a TTY interface, so that a TTY or VDU (the Elekterminal, for instance) can be used for developing programs in machine language.

- With this card added to the existing SC/MP system, it becomes relatively easy to use other CPU cards instead of the original SC/MP card. In this way, the system can be converted to any other microprocessor — the Z80, for instance.

- The main purpose for developing this new p.c. board was to add a cassette interface to the BASIC microcomputer. However, there is also room for a hexadecimal monitor program. This means that programs in both BASIC and machine language can be developed on the same computer. After all, NIBL allows for running machine-language routines as part of a program in BASIC (by means of the LINK command).
The interface

The hardware for the interface consists of an FSK modulator (FSK = Frequency Shift Keying) and an FSK demodulator, as shown in figure 1. When a logic '1' is applied to the input of the FSK modulator, a 2400 Hz sinewave appears at the output. A logic '0' at the input is coded as a 1200 Hz signal. These 2400 Hz and 1200 Hz tones are recorded on tape.

When the tape is played back, the demodulator must obviously convert the 2400 Hz and 1200 Hz signals back to logic '1's and '0's. This digital signal is applied to the serial input (S_in) of the BASIC microcomputer, the software takes care of the conversion from serial to parallel mode and stores the data in the correct memory locations.

Figure 1. The basic principle of the cassette interface described.

2a

Figure 2a. Complete circuit of the FSK (frequency shift keying) demodulator.

2b

Figure 2b. The FSK modulator.
Table 1

| 10 PR "WHAT IS THE FIRST ADDRESS?"; |
| 20 INPUT A; REM INPUT: |
| 30 PR "WHAT IS THE LAST ADDRESS?"; |
| 40 INPUT B; REM INPUT: |
| 50 DO |
| 60 PR A; "="; |
| 70 INPUT C; REM INPUT: # XX |
| 80 A = A + 1 |
| 100 UNTIL A = B + 1 |
| 110 PR "LAST ADDRESS REACHED" > |

Table 1. This BASIC program can be used for loading the machine-language programs by means of a TTY or VDU — such as the Elekterminal.

Table 2. This program can be used for adjusting the modulator.

Table 3. A program that will prove useful when calibrating the demodulator.

FSK modulator

The FSK modulator is virtually a textbook recipe: 'Take one IC ...'. In this case, a function generator — the XFR2206 (see figure 2b). Two supply voltages are required (+5 V and −12 V), both of which are available on the existing supply board. Since the input signal is at TTL logic level, transistor T2 is included as a level converter. The output level is set by means of P4; it should be adjusted to suit the input sensitivity of the recorder used. P2 and P3 set the two output frequencies (1200 Hz and 2400 Hz, respectively). The easy way to do this is to use a frequency counter. However, it is also possible to use the computer itself as a calibration aid, as described in the next section.

Calibrating the modulator

Since all timing in the existing SC/MP system is derived from a crystal oscillator, it is possible to obtain extremely accurate reference frequencies by means of a fairly simple program. Table 1 lists a program in BASIC that can be used in order to load the program given in Table 2 by means of a TTY or a VDU. This second program is used to generate the actual reference frequencies. Once this program has been loaded, it can be started by means of a LINK command. At Flag 0 (pin 14c on the connector) a squarewave will now appear, with a frequency of either 1200 Hz or 2400 Hz. The actual frequency depends on the numbers stored at locations 'AA' and 'BB' (see Table 2). Above this table, the numbers for both locations are given, both for the BASIC microcomputer (4 MHz clock frequency) and for the Elekter SC/MP system (2 MHz clock).

The complete procedure is as follows. When using the BASIC microcomputer, the first step is to load Table 1. As soon as it is started ('RUN'), the computer will ask for the first address; this must be entered in hexadecimal: # 0C00, for instance. Then the last address is entered in the same way. The computer will now proceed to request data for the addresses — note, however, that it will specify each address as a decimal number: 0C00 = 3072. The display will therefore initially read '3072 = ?'; the opcode for the first address can now be entered (# F4). Once the complete program has been entered in this way, it can be started: 'LINK # 0000'. The desired reference frequency will now appear at Flag 0.

The reference signal and that from the FSK modulator are both applied to the simple test circuit given in figure 3. The output can be connected to a high-impedance headphone, or to a tape recorder with some kind of level indicator. The correct numbers are loaded into the program for a 1200 Hz reference tone, and a logic 0 is applied to the input of the modulator. P4 in the modulator is set to maximum. If headphones are used, three frequencies will now be heard: the 1200 Hz reference, the output from the modulator, and the difference (beat) frequency. P2 is now adjusted until the beat frequency becomes zero. When using the level indicator on a tape recorder, P2 and the potentiometer in figure 3 are both manipulated in turn, in such a way that the signal level becomes as low as possible — the reference frequency and the modulator output will then be almost identical.

The program can now be modified to produce the 2400 Hz reference frequency, and a logic 1 is applied to the input of the modulator. Using the same procedure as that described above, P4 can now be adjusted until the two output frequencies are (virtually) identical.

FSK demodulator

The circuit of the FSK demodulator is given in figure 2a. The input signal is first passed to a trigger circuit (inverters N1 and N2). This converts the sine wave output from the tape into a symmetrical squarewave. Two differentiating networks (N4 and N5) produce short spikes at both the positive and negative edges of the squarewave, since N3 is included as an inverter in the feed to N4. These spikes are passed to a low-pass filter R7 ... R9 and C4 ... C6; the voltage across C6 is therefore proportional to the frequency of the input signal. A comparator circuit, consisting of T1 and N6, converts this 'smoothed' voltage to the corresponding TTL logic levels: 0 or 1 for 1200 Hz or 2400 Hz, respectively.

The only adjustment point in the demodulator is P1 in the comparator circuit. For this calibration, a tone generator could be used. Alternatively, a simple program will allow the computer itself to do the work.
Aligning the demodulator

If a 'symmetrical' input signal is applied to the demodulator, the output should also be symmetrical. For the input signal, 'symmetrical' means that 1200 Hz and 2400 Hz signals are applied alternatively during exactly equal periods of time — in other words, two periods of the 1200 Hz signal must be followed by four periods of 2400 Hz, then two at 1200 Hz, and so on. The output will then be a symmetrical squarewave, switching between logic 1 and logic 0.

The program given in Table 3 will produce the desired 'symmetrical' input signal for the demodulator. As before, it can be loaded into the BASIC microcomputer with the aid of the program given in Table 1 — in the Elektor SC/MP system it can be loaded directly, of course. The signal is again present at Flag 0 (pin 14c of the connector). Having connected this signal to the input of the demodulator, P1 is adjusted until a symmetrical output signal is obtained. No complicated measuring equipment is required here: the average value of a symmetrical squarewave that is switching between

Figure 3. Why use expensive test gear? This simple circuit is quite sufficient for all the calibration procedures required!

Figure 4. The EPROM section, with address decoding.
0V and the supply voltage is equal to half the supply voltage. A DC voltmeter is therefore connected to the output, and P1 is adjusted so that the meter reads 2.5V. This will occur over a small part of the range of the preset potentiometer, and the 'ideal' setting is in the centre of this range.

For those who are interested, the test signal consists of two periods of the 1200 Hz signal, followed by four periods at 2400 Hz, then two at 1200 Hz, and so on. This corresponds to a transmission rate of 600 baud.

The EPROM section
Up to four EPROMs, type MM52040, can be mounted on this p.c. board. The complete circuit, including address decoding, is given in figure 4.

The address decoder (IC4) is arranged so that this complete 2K memory section (4 x 128 bytes) of EPROM can be located on any 'page' from 0 to F. One complete page corresponds to 4 K of memory, so the 2K contained on this board only fill the lower half of the page—from address x000 to address x7FF. The remaining lines from the address decoder (corresponding to the remaining half page) can be brought out to the connector by means of wire links. Note that this should not be done if the card is to be used in the original Elektor SC/MP system: those bus lines are already in use.

IC3 is the 'page' decoder; in conjunction with N8 it determines on which page this memory section is to be located. IC4 takes it from there, subdividing the page into eight equal sections of 1/8 K. Both IC3 and IC4 are used as three-to-eight decoders; the selection between the lower eight and the upper eight pages is done by means of wire links, as shown.

The data output lines from the EPROM section are buffered by means of IC10, which ensures that the data only appears on the bus when it is actually required.

The p.c. board
All circuits given in figures 2 and 4 are mounted on a single printed circuit board. This board, with its component layout, is given in figure 5.

Once it has been built and adjusted, as described above, it can be plugged straight into the existing bus of either the BASIC microcomputer or the

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Table 4. Instructions for use for the cassette routines.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>B$164 GNC ST FD (2)</td>
<td>For cassette lines 1 and 2</td>
</tr>
<tr>
<td>B$165 GNC ST FD (2)</td>
<td>For cassette lines 3 and 4</td>
</tr>
<tr>
<td>B$166 GNC ST FD (2)</td>
<td>For cassette lines 5 and 6</td>
</tr>
<tr>
<td>B$167 GNC ST FD (2)</td>
<td>For cassette lines 7 and 8</td>
</tr>
<tr>
<td>B$168 GNC ST FD (2)</td>
<td>For cassette lines 9 and 10</td>
</tr>
<tr>
<td>B$169 GNC ST FD (2)</td>
<td>For cassette lines 11 and 12</td>
</tr>
<tr>
<td>B$16A GNC ST FD (2)</td>
<td>For cassette lines 13 and 14</td>
</tr>
<tr>
<td>B$16B GNC ST FD (2)</td>
<td>For cassette lines 15 and 16</td>
</tr>
</tbody>
</table>

---

Table 5. The complete cassette routines. These are stored in EPROM, as described.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>B$174 LROM</td>
<td>For cassette lines 1 and 2</td>
</tr>
<tr>
<td>B$175 LROM</td>
<td>For cassette lines 3 and 4</td>
</tr>
<tr>
<td>B$176 LROM</td>
<td>For cassette lines 5 and 6</td>
</tr>
<tr>
<td>B$177 LROM</td>
<td>For cassette lines 7 and 8</td>
</tr>
<tr>
<td>B$178 LROM</td>
<td>For cassette lines 9 and 10</td>
</tr>
<tr>
<td>B$179 LROM</td>
<td>For cassette lines 11 and 12</td>
</tr>
<tr>
<td>B$17A LROM</td>
<td>For cassette lines 13 and 14</td>
</tr>
<tr>
<td>B$17B LROM</td>
<td>For cassette lines 15 and 16</td>
</tr>
</tbody>
</table>

---

Table 6. The complete cassette routines. These are stored in EPROM, as described.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>B$184 LROM</td>
<td>For cassette lines 1 and 2</td>
</tr>
<tr>
<td>B$185 LROM</td>
<td>For cassette lines 3 and 4</td>
</tr>
<tr>
<td>B$186 LROM</td>
<td>For cassette lines 5 and 6</td>
</tr>
<tr>
<td>B$187 LROM</td>
<td>For cassette lines 7 and 8</td>
</tr>
<tr>
<td>B$188 LROM</td>
<td>For cassette lines 9 and 10</td>
</tr>
<tr>
<td>B$189 LROM</td>
<td>For cassette lines 11 and 12</td>
</tr>
<tr>
<td>B$18A LROM</td>
<td>For cassette lines 13 and 14</td>
</tr>
<tr>
<td>B$18B LROM</td>
<td>For cassette lines 15 and 16</td>
</tr>
</tbody>
</table>
Figure 5. Printed circuit board and component layout for the cassette interface and EPROM section — the circuits given in figures 2a, 2b and 4.

Parts list for figure 5.

Resistors:
- R1, R7, R14 = 1 k
- R2, R4, R5 = 100 k
- R3, R10, R11 = 1 M
- R6 = 47 k
- R8 = 150 k
- R9 = 470 k
- R12 = 3 kΩ
- R13 = 180 Ω
- R15 = 5 kΩ
- R16 = 4 kΩ

Capacitors:
- C1 = 100 n
- C2, C3 = 2 n
- C4 = 4.7 n
- C5 = 1 n
- C6 = 560 p
- C7 = 82 p
- C8 = 27 n
- C9, C10 = 10 μ/16 V Tantalum
- C11 = 1 μ/16 V Tantalum
- C12 = 100 μV

Semiconductors:
- D1 ... D4 = 1N4148
- T1 = BC1798, BC595B or equ.
- T2 = BC177, BC557, TUP
- IC1 = N1 ... N6 = 4049 B
- IC2 = XR-2206
- IC3, IC4 = 74 (LS)155
- IC5, IC6, IC7, IC8 = MM5204
- IC9 = 74 (LS)10
- IC10 = 81 (LS)155
Software

The cassette routines given in Table 5 can be used, in principle, on any SC/MP system. To ‘dump’ a program, the ‘begin’ and ‘end’ addresses must first be specified. This is not necessary when ‘loading’ a program, since these addresses are already specified on the tape.

If the program is run as it stands, the transmission rate will be 600 Baud. Alternatively, the data at address 1FF5 can be modified for different baud rates: 1E, 50, and FE give transmission rates of 600, 300 and 110 Baud, respectively — when used in conjunction with the BASIC microcomputer, that is (4 MHz clock). The Elektor SC/MP system uses a 2 MHz clock, so that the same data values will give 300, 150 and 55 Baud, respectively. Obviously, other values can be used to obtain different transmission rates, as required.

The first and last addresses, and possibly the data value for the desired transmission rate, must first be stored at the corresponding memory locations. This means that the system must include a simple monitor program, at least. The ‘Kitbug’ monitor from the SC/MP introkit, for instance, or the NIBL BASIC interpreter. Although both of these programs also contain their own in- and output routines, it seemed advisable to include these routines in the cassette software given in Table 5.

This avoids any problems that might occur when incorporating these routines in the program — especially when using it with the NIBL interpreter, since different versions of this program exist. The in- and output routines are located at different positions in the memory!

It is quite possible, of course, to use existing in- and output routines — only a few modifications are required in the program given in Table 5 to specify the new addresses (specifically in the sections under S2 and S4). The new ‘load’ and ‘dump’ routines are based on the original Ebug versions. This means that tapes can be recorded on one system and played back on the other without any problems.

The ‘instructions for use’ of the cassette routines are given in Table 4; this actually shows a Load and a Dump procedure. The first step in the Load procedure is to jump to the cassette routines by means of a LINK instruction (assuming that the BASIC microcomputer is used). Then an L is keyed in, to select the Load routine, and the program will be transferred from tape to memory. The example given also shows what happens when an error occurs.

The same procedure can also be used when the tape was recorded at a different transmission rate; however, the ‘speed byte’ at address 1FF5 must first be modified in this case. For 300 Baud, say, the data value #50 must be stored here. Then the LINK instruction

---

**Figure 6.** This circuit extends the ‘load’ cycle of the microprocessor. It is only required when MM5204-type EPROMs are used in conjunction with an SC/MP system that uses a 4 MHz clock frequency. The BASIC microprocessor, for instance.

**Figure 7.** Printed circuit board and component layout for the ‘slow memory access’ circuit given in figure 6.

**Figure 8.** The p.c. board given in figure 7 is mounted on the CPU card, as shown here.
Table 6. Specifications of the 'scratchpad' on Page 1. The subroutines addresses are also given.

<table>
<thead>
<tr>
<th>Address</th>
<th>Function</th>
<th>Displacement</th>
</tr>
</thead>
<tbody>
<tr>
<td>1FF0</td>
<td>CHECKSUM</td>
<td>F2</td>
</tr>
<tr>
<td>1FF1</td>
<td>BLOCK COUNTER</td>
<td>F3</td>
</tr>
<tr>
<td>1FF2</td>
<td>BYTOUT-BYTE</td>
<td>F4</td>
</tr>
<tr>
<td>1FF3</td>
<td>BIT COUNTER</td>
<td>F5</td>
</tr>
<tr>
<td>1FF4</td>
<td>DUMMY VARIABLE</td>
<td>F6</td>
</tr>
<tr>
<td>1FF5</td>
<td>SPEED BYTE</td>
<td>F7</td>
</tr>
<tr>
<td>1FF6</td>
<td>TEMPORARY SPEED BYTE</td>
<td>F8</td>
</tr>
<tr>
<td>1FF7</td>
<td>NOT USED</td>
<td></td>
</tr>
<tr>
<td>1FF8</td>
<td>NOT USED</td>
<td></td>
</tr>
<tr>
<td>1FF9</td>
<td>NOT USED</td>
<td></td>
</tr>
<tr>
<td>1FFA</td>
<td>END ADDRESS HIGH</td>
<td>FC</td>
</tr>
<tr>
<td>1FFB</td>
<td>END ADDRESS LOW</td>
<td>FD</td>
</tr>
<tr>
<td>1FFC</td>
<td>BEGIN ADDRESS HIGH</td>
<td>FE</td>
</tr>
<tr>
<td>1FFD</td>
<td>BEGIN ADDRESS LOW</td>
<td>FF</td>
</tr>
<tr>
<td>1FFE</td>
<td>SAVED P3 (L)</td>
<td>@FF/@01</td>
</tr>
<tr>
<td>1FFF</td>
<td>SAVED P3 (H)</td>
<td>@FF/@01</td>
</tr>
</tbody>
</table>

SUBROUTINE ADDRESSES

PUTC: 81CE  Send one character to TTY via F0.
GECO: 818F  Retrieve one character from TTY via S3 and send 'echo' via F4.
BYTOT: 812B  Transmit one byte via SOut.
LDBYTE: 8155  Retrieve one byte via SIn.

There is given, followed by the start address 8000, this address was listed in Table 4. Finally, Table 4 gives an example of a Dump procedure. First, the begin and end addresses of the program to be 'dumped' are specified. Then the program is started, by means of a LINK command, either at address 8000 (for autostart) or at 8006 (after specifying the desired transmission rate in address 1FF5). The tape is started, and a D is keyed in – the Dump routine will run and the program is stored on tape.

When using NIBL, the end address of the program to be dumped is found by giving the command `PR TOP'. The computer will respond by printing a decimal number; this must be converted to the equivalent hexadecimal value before it can be specified for the Dump routine. The begin (and end) address depends on the Page used: #111E for Page 1, #2000 for Page 2, #3000 for Page 3, and so on. Page 1 is the only one with a 'peculiar' begin address; the reason is that NIBL uses the first 11D memory locations for storing data.

Scratchpad

To avoid the need for an additional RAM card, locations 1FF0 to 1FFF on Page 1 are used as scratchpad for the cassette routines. The data stored at the various addresses are listed in Table 6. Note that, since this section of memory is used as scratchpad, the end addresses on Page 1 should never be higher than 1EFF (4080 in decimal).

Table 6 also lists the various subroutine addresses, with a brief indication of what they do.

More on the EPROMs

At present, ½ K EPROMs are not as 'readily available' as could be wished. If the MM5204Q proves difficult to obtain, a 2 K EPROM can be used instead. The details of this modification are given further on – note that the same idea may prove useful in many other applications as well!

However, assuming that the MM5204Q is to be used, there is still one minor problem. These EPROMs are not quite fast enough for use in a 4 MHz system such as the BASIC microprocessor! For this application, a minor modification of the CPU card will also be required. Before going into detail, it seems advisable to summarize the various possibilities for the EPROM section:

- four ½ K EPROMs, type MM5204Q.
- When used in combination with the Elektor SC/MP system (2 MHz clock), these can be mounted without any further modifications. For use with the BASIC microcomputer, however, the 'slow memory access' described below must be added to the CPU card.
- one 2 K EPROM, type 2716. This alternative is discussed further on; it is equally suitable, without further modifications, for both 2 MHz and 4 MHz systems.
- one 4 K RAM card, used as ROM. This is the way the software was originally tested, and it works on all systems.

Slow memory access

As explained above, this modification to the CPU card is only required if the MM5204 is used as EPROM in conjunction with a 4 MHz system such as the BASIC microprocessor. In all other cases, it is unnecessary!

The problem is that these EPROMs are not quite fast enough. For this reason, the microprocessor's 'read cycle' must be lengthened slightly. This can be achieved by using the NHOE input: when this control pin is connected to supply common, the SC/MP is 'frozen' so that it maintains the current status. A read cycle (or a write cycle, for that matter) can be extended for as long as required in this way. For the present application, the read cycle must be extended by 250...500 ns. Since Page 0 is fully used for NIBL, there is no need to extend the read cycle there. The write cycle can remain unaltered for all pages of memory.

Figure 9. The actual connections between the 'slow memory access' board and the existing BASIC microcomputer CPU board are clearly shown. Note that the wire link marked 'X' must be removed.
11

Figure 10. Should the MM5204Q prove difficult to obtain, there is an alternative: use one 2716 to replace four 5204s!

Figure 11. Printed circuit board and component layout for the extension circuit given in figure 10.

Parts list for figure 11.

Semiconductors:
IC1, IC2 = 74(LS)125
IC3 = 2716, 2 K byte EPROM
suitable for ±5 V supply. Intel,
National Semiconductor or
Motorola versions are ideal.

Resistors:
R1 ... R3 = 4k7

This read cycle extension can be achieved as shown in figure 6. The NRDS signal from the bus is passed through N1 to N2, where it is gated by the signal on connector pin 30c. This is one of the address lines: it ensures that the NRDS signal is only passed when one of the pages 1 ... 15 is selected – not for page 0, in other words. The output from N2 triggers a monostable multivibrator (N3/N4) that delivers an output pulse of approximately 0.5 μs. The result is that the NRDS pulse is lengthened by this amount. The effect on the software timing is so small that the in- and output routines will still function as intended. No modifications are required in the software.

The four components needed can be mounted on a very small printed circuit board. The board and component layout are given in figure 7. As can be seen in the photo (figure 8), this board is mounted near the connector on the CPU board by means of short wire links. The actual connections are shown in figure 9: they are all either wire links on the existing board or pins on the connector. Note that the wire link marked 'X' must be removed.

An alternative for the MM5204
4 x 5204 = 2716. This may seem peculiar arithmetic, but it is actually a good alternative solution if the MM5204 should prove difficult to obtain. The idea is that four ½ K EPROMs can be replaced by one 2 K version. The
cassette routines given in Table 5 can be located in the first ½ K, and the remaining 1½ K can be used for other software — a monitor program, for instance (currently under development!). As shown in figure 10, the chip select connections to the ½ K EPROMs are decoded by means of eight logic gates. Four of these (N5...N8) re-encode the four chip select signals into the two-bit data required to address IC3. Gates N1...N4 ensure that the EPROM is put out of action when no chip select signals are present. Admittedly, these gates are not strictly necessary; all the control signals required are already present 'somewhere' on the cassette interface p.c. board. However, that little word 'somewhere' is the reason for investing in the two additional ICs. It is now possible to mount the complete circuit on a small p.c. board that simply plugs into the IC sockets on the existing board. No messing about!

In principle, you would assume that any 2 K EPROM labelled 2716 could be used, but unfortunately this is not quite true. The limitation is that the IC must work off a single +5 V supply. The Texas Instruments version, for example, needs three supply voltages; for this reason, it cannot be used in this circuit.

The printed circuit board for the 'EPROM alternative' is given in figure 11. After mounting the ICs and resistors, short and stiff connecting wires must be soldered into place to form the connections to the existing IC sockets on the cassette interface p.c. board. This is where the two rows of holes on each side of IC3 come in. The idea is that each piece of wire is looped through two holes, so that a fairly rigid mechanical construction is achieved (see figure 12). The two rows of wire 'pins' are then inserted into two IC sockets, as shown in the photo (figure 13). The 2 K EPROM is located above the IC socket for IC6; the connecting wires are inserted into one row each of the sockets for IC5 and IC7. The four remaining connections to the extension board are connected to four wire links that carry the chip select signals; this is clearly shown in the photo.

In conclusion

As anyone who is actively interested in the 'hardware' side of microprocessors will know, what is written today may well be out of date tomorrow. This is certainly true when you attempt to design 'general purpose' additions, such as the cassette interface described here. Against all odds, we have tried to make this module suitable for all SC/MP systems... Furthermore, we have attempted to solve the availability problem of EPROMs by offering several alternatives.

If you can build and install any of the variations described here, we will have succeeded in our aim. Even if you only succeed after working out some other alternative according to the principles laid out, we feel that this article is not wasted. After all, the idea is to provide you with a cassette interface! And, provided you can obtain any of the EPROMs listed, this should certainly be the case if you are the proud owner of either of the SC/MP systems described in Elektor.
Liquid crystal displays are an economic alternative to the well known LED. They combine high readability with versatility. As far as amateurs are concerned, however, judgement is still being suspended. This is because, until recently, LCD's have always been difficult to obtain, expensive, and involved complex operation. Now, at last, they are in a new phase of development and the prices have come down dramatically.

A little current leads to a lot of contrast. The world’s most well known optical illusion.

During the past two years LCD displays have been catching up with their LED counterparts. In fact, it almost looks as if LED's will soon be considered old-fashioned. This is hardly surprising, when both types of display are compared. LCD's consume approximately 1000 times less current than LED's. Contrast under bright light improves rather than deteriorates. Furthermore, LCD's are extraordinarily versatile. They can be transparent and allow for great flexibility in size and form.

Before the above advantages could be capitalized on, a few initial problems had to be overcome. This was done successfully with the result that high quality LCD's are being mass produced. They now have a satisfactory lifespan and temperature range.

One beneficial effect of the rising quality of the product is that it is becoming more and more in demand in industry and therefore more readily available on the retail market.

Inside LCD's

A detailed knowledge of the technological background of LCD's is not strictly necessary in order to be able to use them. Readers who take an interest in this particular aspect are referred to the bibliography which is given at the end of this article.

An LCD Display basically consists of two very thin glass plates between which there is a liquid crystal layer some 10μm thick. This layer consists of a crystalline molecular structure. What is essential is that the molecular structure changes under the influence of an electrical field. Depending on the direction in which the molecules are organized, the liquid crystal layer becomes either transparent or reflective. The inside surface of the two glass plates is coated with a transparent, conductive layer and this forms the electrodes. A voltage applied to them creates an electrical field which causes the molecules in the liquid crystal layer to change direction. The plane affected (or segment of a digital display) then alters in transparency.

Figure 1 shows the basic construction of an LCD. The SiO₂ layers given in the figure should be mentioned. These insulate the electrodes from the effects of the liquid crystal and the two polarizers (polarisation filter discs). The alignment of the crystalline structure is such that transparency will not change until a voltage is applied. The organisation of the crystal molecules in the electrical field is shown in figure 1. When an (alternating) current is applied between the two electrodes, the crystal molecules will be arranged horizontally. As can be seen, the lower half has no drive current and so the liquid crystals are in a vertical configuration.

In an unenergized state in a reflective LCD, a vertical and a horizontal polar-
izer are laminated onto the liquid crystal cell at right angles (or 90° rotated) to each other (see figure 2a). Vertically polarized light entering the front of the cell (A) follows the rotation of the crystal alignment as it passes through the cell again rotating 90 degrees, the polarized light passes through the horizontal polarizer to the reflector (E). The light is then returned through the cell again rotating 90 degrees, and passes out of the LCD through the vertical polarizer.

In an energized state, however, (see figure 2b) across one or more of the character segments the crystal molecules in the segment align themselves with the electrical field. Rotation does not therefore occur in the energized segment. The vertically polarized light from the energized segments cannot pass through the horizontal polarizer, but is rather absorbed by it. The segments therefore appear as dark images against a light background. The opposite happens with parallel polarisation filters, the powered segments are transparent and appear as bright images on a dark background.

Things are different when a semi-transparent mirror is used as a reflector (figure 3b). It results in 'transflective' displays which can be illuminated from front as well as from behind. When current consumption is of minor concern, in mains power equipment for example, the light source behind the display can be constantly on. If the surrounding brightness is greater than the light intensity effected by the built-in lighting, the display operates in a reflective manner. If the external brightness is less, 'transillumination' or transmission occurs.

There are also displays which operate exclusively on a built-in light source, that is to say, producing transmission without a reflector (see figure 3c). These are called transmissive displays. Recent developments seem to favour reflective and transflective displays, whereas the transmissive type tends to be pushed into the background. The former types nearly always display dark characters on a bright background, whereas the transmissive type features transparent characters on an opaque (dark) background.

Characteristics
The prime feature of the LED is brightness, while that of the LCD is contrast: the main criterion for readability. Contrast involves a certain light/dark ratio of segment brightness during the ‘on’ and ‘off’ state when the external light is constant and it is seen from the same angle. The ratio is between 1:10 and 1:20. A good example of this effect is the text of this magazine where black and white contrast is sharp. The operational ratios also have an influence on the contrast, especially on the viewing angle and on the triggering

Figure 1. Basic construction of an LCD. The layer of liquid crystal is hermetically enclosed between two glass plates. The glass plates contain transparent, conductive electrodes. As shown in the layout, the direction of the molecules changes under the influence of an electrical field. In combination with the externally adhesive polarisation filters, ‘capizing’ the molecules between the triggered electrodes causes a change in the transparency of the corresponding segment.

Figure 2. According to the position of the polarisation filters the following takes place: Figure 2a. In an unenergized reflective LCD, the segments are transparent when the filters are parallel to each other. Polarized light is rotated 90° by the liquid crystal material. Figure 2b. The triggered segments become opaque (dark) when the filters are at right angles to each other. Rotation does not occur in an energized reflective LCD.
(static or multiplex). The viewing angle is shown in figure 4. LCD displays achieve a viewing angle of up to 160°, where the light/dark ratio is 1 : 3.

The contrast is also dependent on the operating voltage. For maximum contrast a certain field intensity between the segment electrodes and the back plate electrode is required, which relies on a certain voltage. Figure 5 shows the typical voltage curve. When the voltage rises the liquid crystal molecules are realigned gradually. The contrast at a certain voltage depends on the percentage of molecules in the field which have already changed direction. When contrast is at a maximum, this will be around 100%. If the voltage is further increased, the contrast will remain constant rather than increase. This may be a disadvantage if multiplex applications are sought. Contrary to multiplexing LCD's, the shorter 'on' period (analogous to the increase of segment current with an LED) is not compensated.

The level of operating voltage required may be freely chosen. On the one hand it is determined by the basic material used and on the other by the density of the liquid crystal layer. The thinner the layer, the higher the field intensity (at the same voltage level) and the lower the operating voltage required. Nowadays, LCD's are being designed with operating voltages in the region of 1.5 V to 20 V. The contrast curve shown in figure 5 is temperature dependent. At higher temperatures, contrast is achieved at a lower voltage. The curve then becomes more pronounced. If the temperature is low, the opposite happens: the curve flattens out. Again, this may cause problems if a multiplex operated system is used.

The switch-over times of an LCD rely on the voltage and temperature. Figure 6a shows the time lapse of contrast when the LCD is switched 'on' and 'off' respectively. It features a relatively long switch-on delay (a in the figure) of 100 ms, before any change in con-
Contrast takes place. If the contrast is to reach 90% of the maximum value, another 70 ms (s) will be required. When it is switched off, the contrast starts to fade immediately but takes about 230 ms (s) for it to be complete. Depending on the type of material used, the turn-on time with a rising operating voltage, becomes markedly shorter, whereas the turn-off time lengthens only slightly.

Temperature is also an important factor. Generally speaking, when the LCD's are in a warm environment, they switch more rapidly (see figure 6b).

**Lifespan and temperature range**

Both aspects are closely connected. A great deal is known about the lifespan of LCD's, nevertheless, it merits a few words here.

What is a lifespan? It all depends on the type of display used (reflective, transmissive or transfective). A 50% drop in contrast leads to various results. The lifespan is also dependent on the number of operating hours until a failure rate of 50% occurs.

No matter how the lifespan is defined, it is certain that during the past few years great progress has been achieved. A life expectancy of more than 50,000 hours (almost six years of operation!) is now quite normal.

In the early stages of development, problems affected the LCD's resistance to ultra violet light, humidity and foreign debris. As glass plates used to be stuck together with adhesive material, the liquid crystal was not hermetically sealed and therefore had a lifespan of only one to two years. This was solved by the introduction of special laminating material for glass. By coating the plates with a thin layer of quartz, the liquid crystal remains unimpaired and at the same time the electrodes are insulated against it.

More stable substances are now being sought to extend the temperature range and improve the switching times. The chemical stability of a few of the most recent liquid crystals is of such a high quality that it has once again become feasible to use the old adhesive technique. This is an important step on the road towards large surface displays with a view to the alphanumerical LCD's of the future.

Polarizers have not, however, undergone a similar development. Light polarization takes place in a polyvinyl alcohol foil, which is stretched to a maximum and then soaked in an iodated compound. The foil is very thin (25 μm) and must be pasted to a carrier foil. Polarizers tend to bleach in high temperature and humid surroundings, which may result in a loss of contrast. A solution would be an LCD with 'sun glass' (darker polarizer)! By using impermeable protective foil and improved adhesive and solidifying processes, polarizers would then be well protected against humidity.

**Operational and storage temperatures**

As mentioned above, the performance of LCD's slows down when the temperature drops. At temperatures of about -10°C they even freeze up altogether with the result that the liquid crystal becomes a solid. At the other temperature extreme the liquid becomes thinner until it loses its crystal structure. A distinction should be made between the operating and the storage temperature ranges. If the working temperature exceeds its range, the display will become inoperative. It is only when the storage temperature range is exceeded, however, that permanent damage is done.
Liquid crystal material currently in use has a working temperature range with a lower limit between –5°C and –15°C and an upper limit between 40°C and 80°C. The storage temperature range has a lower limit of –20°C to –40°C and an upper limit of 60°C to 85°C (depending on the liquid crystal used).

**Voltage control**

LCD segments are triggered into operation by applying an alternating current. It must be a frequency of over 30 Hz (to prevent the display from flickering). This is essential and it makes no difference whether the electrodes have been insulated against the effects of the liquid crystal or not. If they have not been insulated, the application of a direct voltage will result in electrolysis thereby destroying the electrodes. If the electrodes are in fact insulated, the ions in the liquid crystal are shifted. This breaks down the electrical field and the display fades at once.

If the supply is DC (as in battery powered equipment), an AC waveform will have to be generated by means of an oscillator. To prevent the display from visibly flickering, the frequency range is limited at its lower end.
The upper end is limited by the resistance of the electrodes and the capacitance (the RC time constant) of the segments in the display. In an equivalent circuit, an LCD segment represents the parallel connection of capacitor C and a high-value resistor R. The capacitance is primarily determined by the size of the segment surface. For instance, its capacitance per digit depends on the height of the digit and on the LC material and will be between 150 pF (8 mm digit size, high quality LC) and 4 nF (maximum value for 25 mm digit, standard LC).

The resistance is dependent, among other things, on the segment’s surface and on the quality of the electrodes' insulation. In the above example, the corresponding values for the direct voltage resistance would be 1400 MΩ (8 mm) and 8 MΩ (25 mm high).

If only alternating current is applied, the resistance in the segment may be disregarded. The current consumption will then rely on capacitance and frequency (figure 7). In the case of a display with a very small surface area, it is possible to reach a working frequency of up to 1 kHz; with larger displays, however, there is little point in having an operating frequency of over 100 Hz. Usually, manufacturers indicate an operating frequency of 32 Hz at 50 mV.

How does it work?

The next distinction which must be made is the difference between static operation (direct segment control) and multiplex operation (switched segment control). As its name suggests, static operation provides each segment with its own drive, and one common electrode may be used by all the segments (and usually is). Thus, in this respect it is like the seven segment LED displays (common cathode or common anode).

As opposed to multiplex operation, static operation is uncritical with regard to contrast, tolerance and temperature. Figure 8a shows a simple control circuit for a segment with a push-pull transistor stage. The transistors are part of a CMOS inverter IC, a CD4007 or CD4009, for instance. The inverter receives a square wave of 30-50 Hz at its input and switches at its output between +U₀ and 0 V. The peak value of the alternating current applied to the segment is equal to half the operating voltage.

Capacitors are expensive and take up a lot of space when compared with IC gates, so it would be an advantage if the circuit could be built without any discrete components as shown in figure 8b. After inversion, the square wave at the rear electrode is 180° out of phase with the one at the segment electrode. Between the two electrodes lies an alternating current with a peak value equal to the supply voltage U₀. This principle can be put into practice.
in an elegant manner with the aid of EXOR gates of the CMOS type (for instance, CD 4030 or CD 4070). Figure 9a shows the circuit. A gate is required for every segment. To one of the inputs of each gate and the display common, a constant low frequency alternating current is applied. The other gate input then controls the segments. If there is a logic 1 at the control input the square wave at the segment electrode will be out of phase (with reference to the display common) and in phase if it is a logic 0. This is clearly shown in the diagrams in figure 9b. Because the signals are in phase when the segment is powered, no difference in voltage occurs. When they are out of phase, the AC rises with a difference in potential of twice the amplitude of the square wave (between the triggered segment electrode and the common electrode).

This must of course be taken into account when the supply voltage of the LCD displays is fixed. On the data sheet this is usually given as the effective value of the AC waveform. The effective value of the waveform is equal to its peak value and this is equal to the operating voltage $V_d$ of the CMOS gates. For an LCD specifying an operating voltage of 4 to 6 V the CMOS drive circuit will be fed with 5 V.

**Multiplexed operation**

The threshold values of the LCD contrast curve may also be multiplexed, although this will be limited to a few steps. The reasons for this are:
- The contrast is not pronounced.
- The contrast curve is temperature dependent.
- As opposed to LED's the contrast may not be increased by means of short interval overdrive.
- If the system is direct voltage (as opposed to multiplexed) controlled, these problems are avoided, although

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**Figure 10. Layout of segment and back electrodes in a three step multiplex LCD. The segments at the matrix points are between rows (back electrodes) and the segment group connections (columns). In this example a matrix position (row 1, column 2) has not been used.**
the high number of connections to the display and drive circuit is often a drawback.

Commonly used LCD's include the 'three step multiplex'. In this type up to three segments are attached to a single connection. Figure 10 shows a seven segment display for a three step multiplex operation with a matrix organisation for two digits. This example does not include two matrix points which could be added. If all the matrix points are to be optimally exploited, only $\frac{3}{3} + 3$ connections will be required for 18 segments. The system becomes operative in three chronological stages.

First, all the segments at the back electrode COM 1 are triggered, then those at COM 2 and those at COM 3, after which the cycle starts again. In order to trigger the back electrodes (COM = 'rows' in the matrix) and the segments groups (columns in the matrix) square waves are used which supply an AC to the triggered segment. Furthermore, the control signals have to be such, that the AC is in phase for the 'on' segments and out of phase for the switched 'off' segments. The row and column signals have to differ in amplitude. Usually, the higher voltage is applied to the back electrodes and the lower to the segments. Figure 11 gives a practical example of digit 4 which is lit in the seven segment display shown in figure 10. The triggered segments are given in the matrix as shaded circles.

The corresponding pulse schedule shows from top to bottom: clock, COM signals, column signals and the differential signals UCOM - UCOL which become operative in segments dp, nc, G and C.

One multiplex step corresponds to one clock period. The column signals are obtained when a square wave is connected through to a clock signal, and to an equal voltage for the rest of the time (the two subsequent clock periods) to the COM row concerned. The pulse at the COM transmission activates the rows concerned. Whether the segments on the row (matrix points) are 'on' or 'off', depends on the phase layer of the column signal at that moment. For an inoperative point, the column signal is in phase and for a triggered one it is out of phase to the column signal. In the pulse diagram, for example, column signal COL 1 is out of phase to the common signal COM 1 during the first multiplex step (pulse on COM 1). The decimal point (dp) is switched 'on' during the first step. This can also be seen in the differential signal (COM 1 - COL 1).

The voltage operated at both segment electrodes is added to the COM and COL signal. This is not true of the untriggered segment nc on the first row. Here the column signal COL 2 is in phase to the COM 1 signal.

Photo 3. Alphanumeric 48 digit LCD unit. This ready-to-incorporate module by GEET has a display surface of 142 x 22 mm, two rows of 24 digits, where every digit is made up of a 5 x 7 matrix (a total of 1680 display dots). The module already contains a multiplex drive circuit and consumes 2 mA. The printed circuit at the rear of the display module is optional and has a character generator, ASCII input bus and display interface.

result at segment nc is an AC which is definitely smaller than at the triggered dp segment, because the COM and COL signals are now subtracted. The value of the AC remains below that of the minimum operating current of the LCD. The untriggered segment will of course not be activated. The column signal is generated by means of a shift register, at each output of which an EXOR gate has been connected. The second input of all the EXOR gates is at the clock for direct triggering. This is how the information ('1' or '0') at the shift register's output can determine the state of the square wave at the output of the EXOR gate (inverted or non-inverted). After the gates, CMOS analogue switches follow, which switch the voltage values when

Photo 4. Liquid crystal screen in a pocket TV prototype manufactured by National Panasonic (Matsushita). With a total of 57,000 display dots it operates on a supply of 4.8 V.
the column signal is generated. The optimum ratio of row voltage to column voltage is $V_{opt} = \sqrt{n}$ where $n$ is the number of multiplex steps. For a three step multiplex the ratio is $\sqrt{3} \approx 1.73$. Figure 12b shows the required voltage values for three step multiplexes to be generated and the corresponding voltage phase for the COM and COL signals. The voltage $U_o$ is the starting voltage (for a 10% contrast) of the display and this is indicated on the data sheet. Usually 1.05 V is enough.

**Conclusion and outlook**

More information can now be displayed with multiple segment displays. They are available with 1120 light spots (32 alphanumeric characters in a 7 x 5 format). The portable, battery driven data terminal with an LC screen is no longer an illusion. The complex control systems behind such displays can be simplified by means of integrated drive circuits. As the number of multiplex steps in LCDs is, technologically speaking, limited, the LCD has to become ‘active’ for average to large quantities of information. That means that at every intersection of the control wires there is an active semiconductor element, such as an FET.

The rear of the display consists of a large area chip, on which the corresponding transistor matrix has been etched.

A display of this type was recently introduced by National Panasonic (Matsushita). It was demonstrated in a prototype pocket TV set with a flat LCD screen (see photo). The reflective LCD contains 57,000 (240 x 240) displays dots on a chip measuring 44 x 56 mm.

Figure 13 shows the basic construction of the screen. Every matrix point on the silicon substrate consists of a capacitor and of an FET. 110,000 transistors and capacitors on a single chip! The sample TV consumes barely 1.5 W with a battery voltage of 4.6 V (2 lithium cells). It is not likely to be mass produced until a further reduction in dimensions and current consumption has been achieved. At any rate, the example shows that a flat LCD screen...
Figure 12. The required voltages for a three step multiplex and corresponding voltage levels of row and column signals.

can already be produced at this stage in development. As far as multi coloured LCD's are concerned, however, the production of these cannot be expected in the near future.

Sources:


Figure sources:
Figures 1, 4, 6a, 7, 9b, 11, 12b, photo 2 and photo 3: Siemens.
Figures 2a, 2b Fairchild Camera and Instrument Corp.
Figure 6a: VALVO.
Figure 10: Data Modul.
Figure 13, photo 4: National Panasonic/Matsushita.
Photo 1: HAML IN

Figure 13a. The construction of the ‘flat’ LCD TV screen.
Figure 13b. The ‘electronic components’ in each matrix point of the TV screen.
flexible intercom system

P. Deckers

Certain requirements must be met if an intercom system is to be fully flexible and efficient. It is essential that any station can call any other without the need for a master station. The number of interconnecting wires should be as few as possible, conversations between any two stations should remain private and the standby current drain needs to be low. It would be useful if the system could also operate as a babyphone without blocking the line. The intercom described here complies with all of these requirements while being flexible with regard to station location.

Number 98 in the Elektor Summer Circuits (1979) issue reached a respectable twelfth position in our recent competition, reason enough for us to look at the circuit in greater detail. It achieves everything that is required of an intercom system and consequently remains unchanged. (A minor error did occur in the original circuit diagram, however, resistor R20 should have a value of 1Ω and not 1kΩ.)

The intercom system is designed to have a maximum of five stations with complete security between any two. Furthermore, any station can be wired as a babyphone. The system operates on a four-wire ring cable laid in any convenient manner, that is, two or more stations can be connected in the same length of cable 'in series', or individually by a 'spur', or in any combination that happens to fit the 'bricks and mortar'. For further flexibility suitable sockets can be placed in any desired position and if all stations are equipped with plugs they then become completely mobile. The only criterion in the cable network is that the power supply should be connected to it - at any convenient point.

Block diagram

Figure 1 shows the block diagram of one of the stations (number two) together with the power supply. The four wires of the ring circuit carry the positive and negative of the 15 volt supply, the audio signal, and the control signal (S). Depending on the station's number, one of four reference voltages can be switched to the control line while a fifth reference voltage is connected directly to one input of a window comparator. In this way the

![Diagram](image_url)
Comparator will receive the second lowest reference voltage from station 2 and the highest, for instance, from station 5. As the comparator’s other input is connected to the control line, when the voltage on the S line is the same as the reference voltage for a particular station, the electronic switch (ES) will close thereby supplying power to the preamp and power amplifier stages. When inoperative, the ‘push-to-talk’ switch (S2) will be in the ‘listen’ position, so that the audio signal on the LF line reaches the loudspeaker by way of S2 and the power amplifier. To reply, push-button S2 is pressed. The loudspeaker will then be connected to the input of the preamp and will function as a microphone. The output signal of the preamp is fed to the LF line via S2b. When there is a reference voltage on the S line, a ‘line busy’ indicator will light at every station. To call a particular station, the corresponding key (S1a…S1d) is pressed causing the relevant reference voltage to appear on the S line. As all five switches are mounted in an interlocking group, pressing one of the keys S1a…S1d will cause the S1e key to drop out and to feed the amplifiers with supply voltage. Again, S2 can be used to switch between transmission and reception. As the S line voltage no longer corresponds to the reference voltage of the station itself, the ES switch will remain open.

Circuit diagram

Figure 2 shows the complete circuit diagram of one station. The five reference voltages are derived from the supply via five zener diodes (D1…D5) which are connected in series. Resistor R1 ensures that a current of approximately 12 mA passes through the zener.
Figure 3. The printed circuit board and component layout for one station.

Parts list

Resistors:

<table>
<thead>
<tr>
<th>R1</th>
<th>390 Ω</th>
</tr>
</thead>
<tbody>
<tr>
<td>R2, R3, R7, R8</td>
<td>47 k</td>
</tr>
<tr>
<td>R4</td>
<td>470 Ω</td>
</tr>
<tr>
<td>R5, R6, R9, R24</td>
<td>10 k</td>
</tr>
<tr>
<td>R10</td>
<td>2k2</td>
</tr>
<tr>
<td>R11</td>
<td>68 k</td>
</tr>
<tr>
<td>R12</td>
<td>8k2</td>
</tr>
<tr>
<td>R13</td>
<td>680 Ω</td>
</tr>
<tr>
<td>R14</td>
<td>220 Ω</td>
</tr>
<tr>
<td>R15, R21</td>
<td>56 k</td>
</tr>
<tr>
<td>R16</td>
<td>470 k</td>
</tr>
<tr>
<td>R17</td>
<td>39 k</td>
</tr>
</tbody>
</table>

Capacitors:

| C1, C2, C3 | 100 n |
| C4, C5, C9, C10 | 10 μ/16 V |
| C6 | 560 p |
| C7 | 56 p |
| C8 | 2μ2/16 V |
| C11 | 47 n |
| C12 | 27 |
| C13 | 220 μ/16 V |
| C14 | 470 n |

Semiconductors:

| D1, ..., D5 | BZX75C2V1 (or green LED, or 3 x 1N4148 in series) |
| D6, ..., D9 | OA85, DUG |
| D10, D13 | BZX75C1V4 (or red LED, or 2 x 1N4148 in series) |
| D11 | LED |
| D12 | 1N4148, DUS |
| T1 | 2N2905A |
| IC1a, b, c | TCA220 |
| IC2a, b, c | TCA210 |

Miscellaneous:

| S1a, S1e | 5 key interlocking switch |
| S2a, b | double pole pushbutton switch |
| S3a, b | double pole on/off switch |
diodes. The call up voltages for the other four stations are selected by means of switches S1a...S1d via diodes D6...D9. The remaining reference voltage is connected to the junction of diodes D12 and D13 which form one input of the window comparator (IC1a and IC1b). As it is station four which is shown in the diagram, the reference voltage will be 8.4 V (4 x 2.1 V). Therefore the voltage levels on the non-inverting inputs of IC1a and IC1b are 7 V and 9 V respectively. When the voltage on the S line is somewhere between these two levels, the output of IC1a will be low and the output of IC1b will be high. Transistor T1 (the electronic switch of figure 1) will start to conduct thereby providing the preamp and power amplifier (IC2a and IC2b respectively) with power. A call up voltage greater than 1.4 V on the S line is detected by IC1c which will light D11 to indicate that the audio line is busy. S1a...S1d together with S1e form a row of interlocking keys. When one of these is pressed, any key which was depressed earlier will spring back. Switches S1a...S1d are wired so that contact is made when they are depressed. Switch S1e on the other hand, is wired so that contact is made when it is not depressed (‘other way round’). S1e should be depressed whenever one has no intention of conveying a message. This will put the unit in the receiving or ‘listen’ mode.
Switch S3 enables the intercom to be used as a babyphone. In the baby’s room the intercom station is switched over to ‘babyphone’. This makes the unit’s preamp slightly more sensitive since it bypasses resistor R24. The output of the preamp is fed continuously to the LF line via S3b. Every other station can ‘listen’ to this room simply by pressing the corresponding button (and at the same time conversation can be held between the other stations as normal).

Construction and setting up
The printed circuit board and component layout for the flexible intercom are shown in figure 3. There are four mounting holes in the centre of the board, apart from the ones at each of the four corners. This enables the board to be mounted as a single unit. An alternative is to saw it in half and mount the two halves one on top of the other. Both halves are connected by a pair of wires. During construction the correct positioning of diodes D6...D9 and the connection of the key switches must not be omitted. This is because there are five reference voltages and only four connections to the S line. The fifth is connected directly to the junction of D12/D13. This means that there is one wire link on each of the five printed circuit boards and in each case it is in a different place. The power supply can be virtually any 15 V/1 A type. A suitable circuit is shown in figure 4. The power supply can be connected to the ring cable at any convenient point.
It should be mentioned that the 2.1 V and 1.4 V zener diodes may prove difficult to obtain, in which case they may be substituted for green LEDs and red LEDs respectively. For this purpose the LEDs should be forward biased. Resistor R12, the pull down resistor on the control line, is only required in one of the stations. The intercom system needs very little adjustment. The sensitivity control R1, should be adjusted while the circuit is switched to the babyphone mode, whereas the sensitivity of the intercom during normal operation is determined by the value of R24.
Anti-slippering material

When you're trying to repair a tiny mechanism on the workbench, it keeps slipping away; or you want to solder a connection on a circuit board, which won't stay put, or you must adjust a delicate instrument, holding it in place while doing so without leaving vise-marks on it - how do you solve these problems?

The answer in these and similar situations may well be an anti-slip material, produced by Spirig (Switzerland) and now available in the UK from Cobonics Ltd., London. Called 'StopSlip' elastomer, these high-friction flexible mats come in two thicknesses: 1 mm and 2 mm - and any desired dimension up to 1 meter (3.2 ft) square. The 1 mm material, which can also be ordered in roll lengths, is produced only in a deep blue; the 2 mm mats are available in three additional colours: green, red and yellow.

What makes a StopSlip elastomeric pad so useful is its incredibly high coefficient of friction. A piece of StopSlip material can be brought very close to vertical, and flat objects simply placed on it - not stuck on - will stay in place.

Tackiness of the StopSlip mats is inherent in the material; it does not gradually decrease, nor is it affected by repeated wet-mapping.

Cobonics Ltd., Knappton Mews, Seely Road, London SW17 9RL. Telephone: (01) 767-6780

A new generation of cases

The Bocon range of instrument cases from West Hyde Developments have been widely acclaimed for their impeccable tooling, and the latest two types will certainly be no exception.

The Bocon 'Desk' series is made in black ABS in four sizes. These beautiful mouldings have an ingenious stepped tongue and groove construction, with highly polished surfaces and flat, textured areas on the top. The one-piece front panel is natural anodised aluminium, angled to provide three separate surfaces. Inside there is provision for p.c. boards or chassis.

The Bocon 'Commander', BOC 690, is a keyboard and display enclosure made in black foam plastic. The housing is designed to accept most proprietary keyboards, and has a similar finish to the rest of the Bocon range.

The front and rear panels are natural anodised aluminium, completely flat, and the rear aperture will accept a 19" rack frame 34" high. There is a second smaller 'Commander' Model BOC 680, for keypads and smaller displays, constructed as two clip-together halves in black ABS. The two top anodised panels are also flat and the base incorporates four brass inserts to support a standard Eurocard.

West Hyde Developments Ltd., Unit 9, Park St., Ind. Est., Aylesbury, Bucks HP20 1ET. Telephone: (0296) 20441

The word's lowest noise audio preamp IC?

Claims based on the above superlative tend to be taken with a pinch of salt, particularly in the electronics business, where such a state of affairs may be very short lived. However, the HA12017 now in stock at AMBIT is sufficiently superior device to warrant such billing.

The SIL housed IC uses Hitachi's new silicon surface process, to provide an exceptional low noise characteristic, that is reliable repeatable in mass production. Under standard pickup conditions, a S/N ratio of 82.6 dB is achieved, with an equivalent input noise of just 0.185 µV.

However, as well as this phenomenal feature the IC has less than 0.002% THD over the audio band of 20 Hz to 20 kHz, at an output level of 10 V (RMS) - again with the measurement carried out under RIAA conditions. The SIL package means that isolation between stages is kept to a maximum.

Ambit International, 200 North Service Road, Brentwood, Essex, CM14 4SG. Telephone: (0277) 230009.
Improved capacity range tuning diodes

Toko has now introduced the new KV 1235 and KV 1236 range of high capacity ratio tuning diodes. Both types employ the unique 'snap-apart' principle of packaging, that enables close tolerance matching of multiple diode arrays to be achieved whilst maintaining full layout flexibility, with individual tuning diode packages. The new diodes have a guaranteed 16.8:1 tuning capacity range with only 1...9 V DC tuning bias required, and are supplied in only two ranks of basic min/max capacity values. The actual swing is typically from 50 pF to 500 pF (20:1), allowing for a great deal of stray capacity and general circuit tolerance in radio designs.

The diodes are available in matched triplets (KV1235), or pairs as illustrated (KV1236). Ambit is stocking these device, together with applications data covering a revolutionary new technique in radio design that virtually eliminates tracking error and stray electrostatic pickup on antenna connection wiring.

Ambit International, 200 North Service Road, Brentwood, Essex, CM14 4SG. Telephone: (0277) 230909

Semi-automatic half panel turntable units

Symot Limited announce that two models of a semi-automatic half-panel turntable are now in production: the model FX.201 DR incorporating a coreless type direct drive motor, and the model FX.201 XR, a belt drive model incorporating a frequency generator servo motor. Both models have a mechanically similar appearance and incorporate a high quality 12” aluminium die cast platter, rubber mat and 50/60 Hz strobe markings on the periphery. The tone arm is of high-quality, low-mass, tubular S-shaped construction, incorporating a removable headshell. The arm has an oil damped cue control and an adjustable anti-skating device. The tracking force is controlled by a removable and adjustable counter weight. Both models incorporate an automatic return and stop function initiated at the end of record play by the relative position of the tone arm: Wow and flutter is typically 0.06 wrms (DIN) for the direct drive model and 0.075 wrms (DIN) for the belt drive model. Rumble for both models is typically better than 65 dB.

Lightweight vise

Ideal for both professional and amateur workshops, as well as laboratories and field service engineers, OK's new VV.1 light duty vise has been designed for precision handling of small components and assemblies. Not only would it be fixed to work surfaces by a lever-operated suction mechanism but where permanent installation is required it can be screwed down. The 1/2” (38 mm) wide jaws have 1/16” (32 mm) travel controlled by a large knob for precise positioning. The body is moulded from tough ABS, with built-in fixing lugs, and the unit is light enough to be carried in a tool kit. Priced at £.320 including VAT and postage.

OK Machine & Tool (UK) Ltd, Dutton Lane, Eastleigh, Hants S05 4AA, Telephone: (0703) 610944

New Trio oscilloscope offers high technology at low cost

The Trio Model CS.1830 oscilloscope has been introduced to meet an extremely broad spectrum of signal measurement requirements, and offers a comprehensive range of user facilities for the display of audio, video, pulse and digital signals within a band width from DC to 30 MHz. These include sweep and trigger display functions, which assist in the detailed analysis and measurement of complex waveforms, and a vertical sensitivity of only 2 mV per division.

The CS.1830 uses a domed mesh PDA rectangular c.r.t. of 120 x 96 mm, which incorporates an internal graticule. This type of c.r.t. also provides extreme brightness and clarity of display and introduces minimal parallax distortion. Other features include an automatic synchronisation system, which eliminates the need for problematical sync. determination procedures; a "single sweep" facility, for the measurement of single pulse waveforms; a "hold-off" function, which ensures stable synchronisation for highly complex video or logic waveforms; and "auto free-run", which assists in voltage measurement and the detection of input signals. Additionally, delayed and normal sweeps are selectable for both display channels, and Lissajous patterns may be used for the measurement of phase differences in the signals input to the two channels. Its input impedance is 1 MΩ, and it will accept signal inputs at up to 600 V peak-to-peak or 300 V DC. Accessories include 2 complete probes, power cable and operators manual. It is priced at £.455.

House of Instruments, 34/36 High Street, Saffron Walden, Essex, CB10 1EP. Telephone: (0799) 22612
DC switched and tuned AM radio unit

The new 91072 from Ambit International is available, in three stages of complexity, culminating in a four band unit that can be (uniquely) both switched and tuned by DC connections only.

Switching uses a 'ground-to-make' system, enabling easy control from MPU bus lines if required.

The standard bands are: Longwave 150-400 kHz, Mediumwave 510-1620 kHz, SW1 5-10 MHz and SW2 1.64 MHz.

Any frequency span of approximately up to 3:1 ratio can be accommodated in the region 100 kHz to 30 MHz to special order.

The unit is intended for broadcast radio reception, and is fitted with a 6-8 kHz bandwidth multi-element ceramic filter. A buffered local oscillator output, together with DC switch off through a high impedance drive is also available.

Tuning a complete 3:1 frequency span is achieved with only 1...9 V bias, thus the unit may readily be interfaced with any Ambit tuning synthesiser systems.

The board is normally supplied in a screening can, with edge connector terminations, or may be supplied as a bare PCB for incorporation into larger enclosures (as illustrated).

The antenna for LW/MW is a ferrite rod, but the other two bands are intended for long wire termination. If required, the two SW bands may be substituted with wire fed MW/LW.

Ambit International, 200 North Service Road, Brentwood, Essex, CM14 4SG. Telephone: (0277) 230909

Portable capacitance meter

The new Model 820 portable capacitance meter from Havant Instruments Limited is an economical multi-range instrument combining digital accuracy with complete portability. Its ten ranges cover capacitances from 0.1 pF to 1 Farad. Accuracy is 0.5% or 1% of full scale, and resolution down to 0.1 pF, according to range.

In use the capacitor leads are simply inserted into a pair of slots and the capacitance is indicated on the clear 4-digit LED display. A flashing display provides overrange indication. Provision is also made for using jack plugs when measuring in-circuit capacitances.

The Model 820 is ideal for production line or laboratory use. It has a robust and attractive moulded case but weighs only 675 g (1.5 lb). It will operate with rechargeable or disposable cells and there is provision for a charger. A tilt stand, spare fuse and 28-page operating manual are supplied.

Havant Instruments Ltd., Unit 3, Westfields, Portsmouth Road, Horndean, Hants.
Telephone: (0705) 596020

4½-digit multimeter

Gould Instruments Division has introduced a new 4½-digit multimeter, the DMM12, which features a liquid crystal display, a measurement accuracy of 0.05% and a built-in electronic technique for making true root-mean-square (r.m.s.) measurements on AC signals. Using the latest solid-state circuitry and components specifically selected for high stability and low-noise performance, the DMM12 has 27 measurement ranges for AC and DC voltage, current and resistance, and is also available with optional probes for radio-frequency and high-voltage measurements.

The Gould DMM12 digital multimeter has an ergonomically designed front panel using the latest international symbols. Maximum reading is 19999, and maximum resolutions on current, voltage and resistance measurements are 10 μV, 10 nA and 100 mΩ, respectively.

The liquid crystal display incorporates separate positive or negative polarity indication plus a decimal point. Overrange and 'batter low' are also indicated using the display. The true r.m.s. sensing AC/DC converter used in the DMM12 can accept waveforms with a crest factor (peak/r.m.s. ratio) of up to 4:1 at full scale, and a combined AC/DC facility is available to measure AC waveforms with a DC content. The true r.m.s. value measures the energy content of an AC waveform, and hence makes the DMM12 ideally suited to power-system measurements. The DMM12 is housed in a rugged case and meets IEC348 and VDE specifications.

Standard models are mains line powered but option BP12 gives true portability with rechargeable cells.

Gould Instruments Division, Roebuck Road, Hailsham, Essex, 1G6 3UE Telephone: (01323) 500100

A new magnetic tape head

Being introduced by Monolith Electronics is a new magnetic tape head for compact cassette machines. The C44RP2ES01 is a four channel cassette head for record and playback, which also has combined two independent half track erase sections, thereby providing for full stereo autoreverse record/playback and erase all in one unit.

This head is produced to the standard "EIAJ" mounting format, making it suitable for most tape transports, having 17 mm spaced mounting holes, and measuring 12 mm from hole centres to front face. Wiring is facilitated by the use of a printed circuit board mounted to the rear.

Each record/playback channel has an impedance of 650 ohms at 1 KHz, with a head gap in the order of 1.5 microns giving a playback frequency response of +10 dB over the range 8 KHz/333 Hz. The erase sections have an efficiency of better than 55 dB on a 1 KHz signal.

The C44RP2ES01 was designed for autoreversing stereo recorders, but may also be suitable for certain data recording purposes.

Monolith Electronics Co. Ltd., 5/7 Church Street, Crewkerne, Somerset TA18 7HR, Telephone: (0460) 74321
Miniature chokes
The new 8RBS series of fixed inductors adds a fourth member to TOKO's range of signal chokes. The 8RBS spans from 100 μH to 15 mH in a diminutive package, based on 5 mm pin spacing, with Q's as high as 80.

Economy wire stripper and cutter
A new simple-to-operate wire stripper and cutter has been introduced by AB Engineering Company. Known as the AB MK 001, it features a knurled knob adjustment to control the stripping depth, a retaining clip to ensure it remains in the closed position in the tool box or pocket and a curved cutting edge which provides a secateur-like action for clean wire cutting. Based on well-proven AB MK 100, the new MK 001 has an improved locking device and is priced at £1.85.

Wire wrapping kit
Ideal for small scale production, field service or hobby use, OK Machine & Tool (UK) Ltd's 'Just Wrap' Kit complements the new 'Just Wrap' wire wrapping tool. The tool wraps 30 AWG (0.25 mm) wire onto standard 0.025 square posts without stripping or slitting the insulation and can 'daisy chain' continuously through several points or be used for point-to-point wiring. It contains a built-in wire cut-off device for terminating the final connection of each chain. The JWK-6 Kit contains the tool plus the JJW-1 unwrapping tool and four 50ft wire refill cartridges each in red, white, blue and yellow, all packed in a sturdy, re-usable clear plastic box.

Weston 6000 autoranging multimeter
This compact, rugged, lightweight instrument combines the accuracy and convenience of digital readout and measurement hold with the broad range coverage of conventional VOM's. 3½ digit, 1½” high reflective liquid crystal display gives excellent readability over a wide range of ambient light levels. The autoranging facility means that 26 ranges of measurement are available with only two switches to select functions. Two disposable 9V transistor batteries offer more than 200 hours of continuous operation. A carrying handle serves as a tilt stand and a display window protector. The unit is supplied with leads in a strong carrying pouch — ideal for tool kit use. DC and AC volts 200 mV, 2 V, 20 V, 200 V and 1000 V ranges. DC and AC current 2 mA, 20 mA, 200 mA, 2 A, 10 A ranges. Resistance ranges 2 KΩ to 20 MΩ. Typical accuracy 0.5%. Overall dimensions 7” x 5½” x 2½”. The Weston 6000 Multimeter with pouch costs £145.00 + VAT.

Miniature encoder switch produces BCD signals
A miniature rotary encoder switch, having 10 positions and a four line binary coded decimal output, has been introduced by Impeutron Limited. Measuring only 23 x 23 x 19 mm overall (excluding mounting pins and spindle) the BCM 23 switch will enable designers to fit a low cost encoder into tight corners. The device will prove particularly useful where manual controls or mechanical assemblies have to be interfaced to electronic logic. The unit has five signal pins and two fixing tags for PCB mounting, and may also be front panel mounted using a threaded spindle bush and nut. One signal pin is for voltage supply, while the others represent 1, 2, 4 and 8. On position '0' no contact is made, but as the spindle is clicked around its remaining 9 positions the four output pins represent the number of the position selected in BCD form. The spindle may be continuously rotated, and in this mode may be used as a shaft encoder with 36° resolution. The BCM 23 is manufactured to high standards, although it is a relatively low cost device. Rated operational life is greater than 10° rotations, (2 million steps) and operating torque is as low as 500 gcm. The electrical rating of the switch is such that it may be directly connected to substantial current drains. Power rating is 3 W maximum, with maximum voltage and current ratings of 200 V and 500 mA DC.

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